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Schematic Document

Crestline + ICH8M

2008 / 02 / 18 Rev:0.3

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Issued Date	2007/12/28	Deciphered Date	2007/12/28	Title: Cover Sheet
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				Date: Monday, February 18, 2008; Sheet 1 of 53

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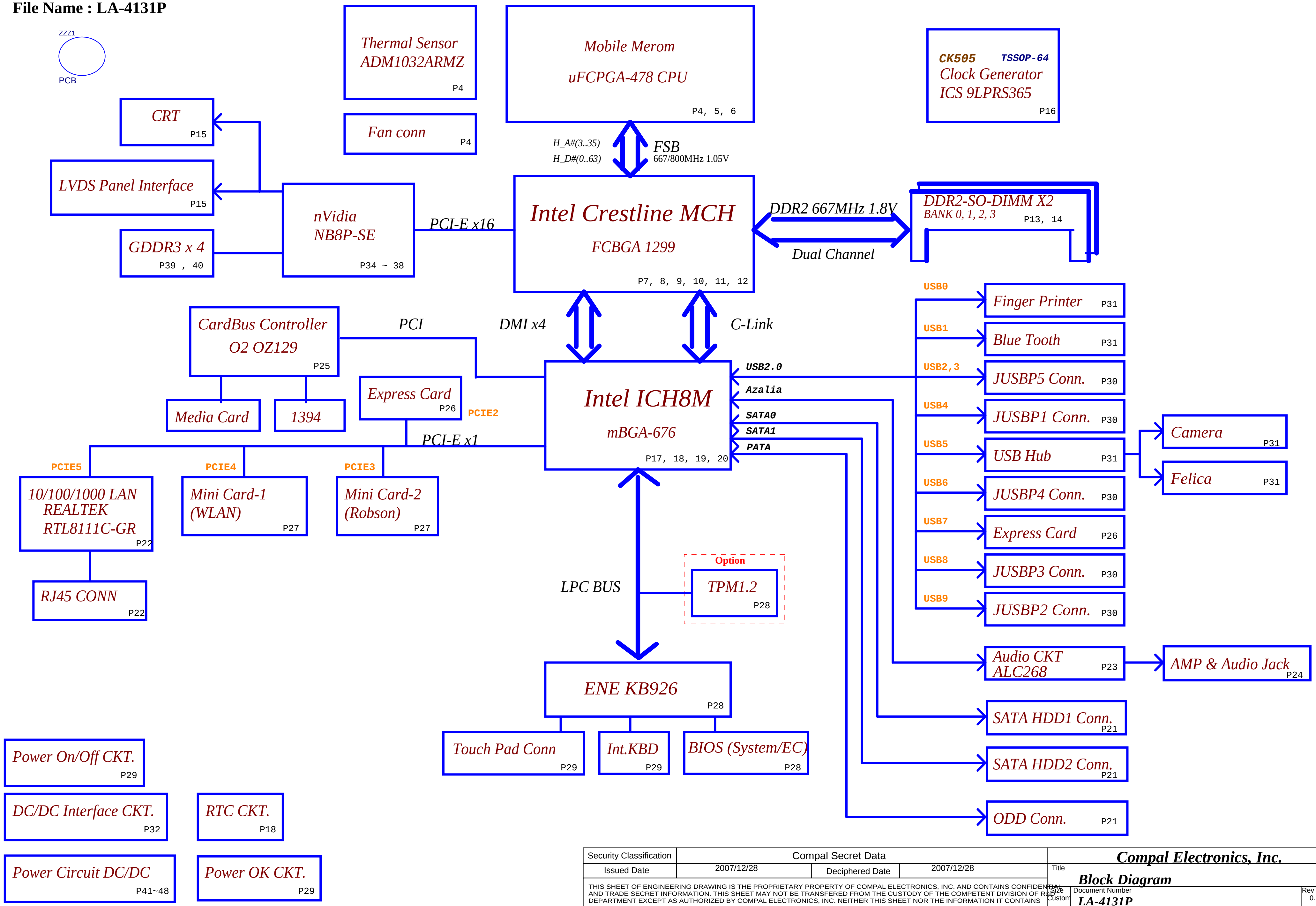
Project Code: ANRJAL6000 (JAL60)

File Name : LA-4131P

JAL60 UMA / Discrete



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Issued Date	2007/12/28	Deciphered Date	2007/12/28	Title: **Block Diagram**
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Voltage Rails

O MEANS ON X MEANS OFF

Power plane / State	+B	+5VALW +3VALW	+1.8V	+5VS +3VS +1.8VS +1.5VS +1.25VS +1.2VS +0.9VS +CPU_CORE +VGA_CORE +VCCP	CLOCK
S0	O	O	O	O	O
S3	O	O	O	X	O
S5 S4/AC	O	O	X	X	O
S5 S4/ Battery only	O	X	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X	X

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

Board ID Table for AD channel

Vcc	3.3V +/- 5%
Ra / Rc	100K +/- 5%

Board ID	Rb / Rd	V_{AD_BID} min	V_{AD_BID} typ	V_{AD_BID} max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

O MEANS ON
X MEANS OFF

S3 : STR
S4 : STD
S5 : SOFT OFF

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	
2	
3	
4	
5	
6	
7	

BTO Option Table

BTO Item	BOM Structure

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD21	0	PIRQG

EC SM Bus1 address

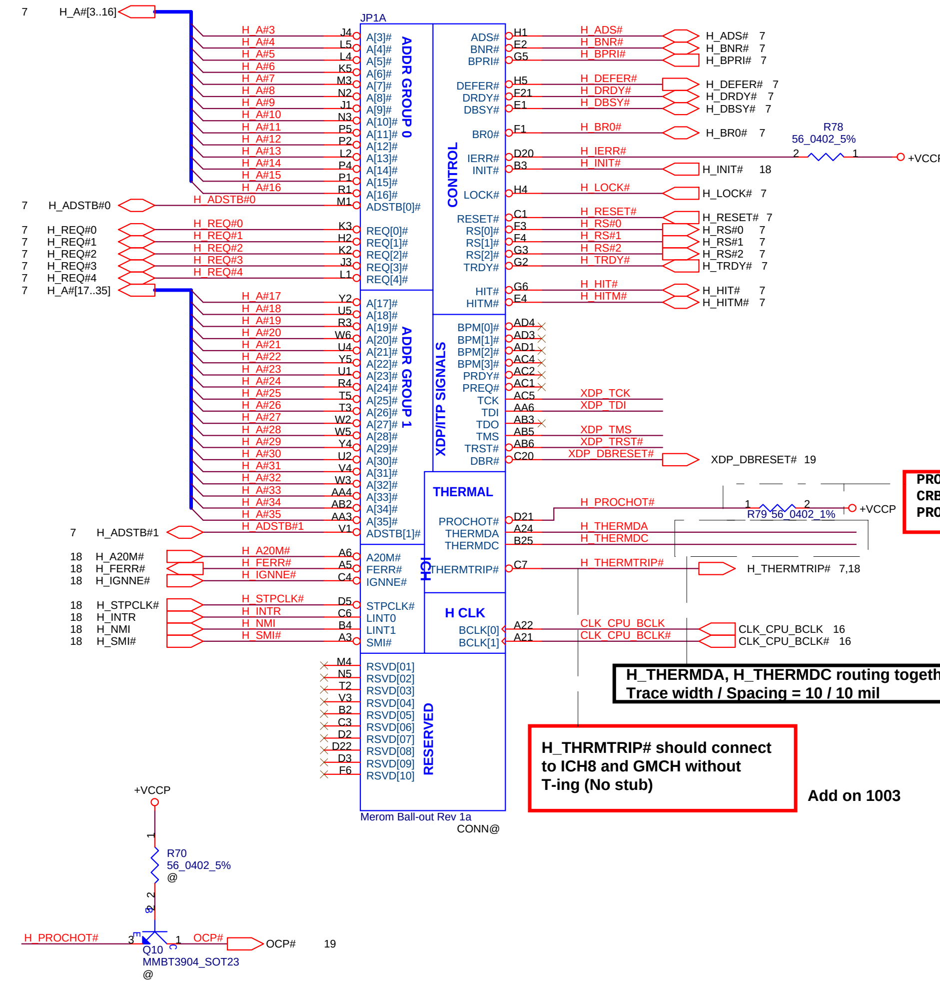
Device	Address
Smart Battery	0001 011X b?
EEPROM(24C16/02)	1010 000X b?
(24C04)	1011 000Xb?

EC SM Bus2 address

Device	Address	
EMC1402-1-ACZL-TR_MSOP8	4C	for CPU Thermal Sensor

ICH8 SM Bus address

Device	Address
Clock Generator (ICS ICS9LPR365)	1101 001Xb?
DDRII DIMM1	1001 000Xb?
DDRII DIMM2	1001 010Xb?



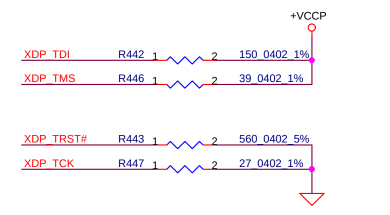
PROCHOT# is not used ---> 56 ohms pull-up
CRB uses 1K ohms pull-up resistor to fix
PROCHOT# failure when driven by a thermal sensor.

H_THERMDA, H_THERMDC routing together,
Trace width / Spacing = 10 / 10 mil

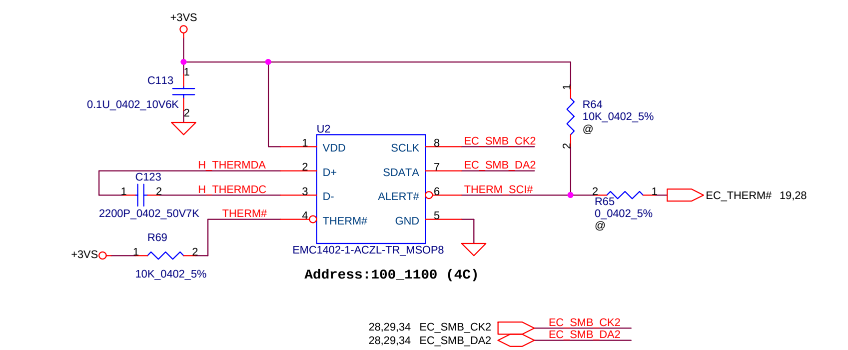
H_THRMTRIP# should connect to ICH8 and GMCH without T-ing (No stub)

Add on 1003

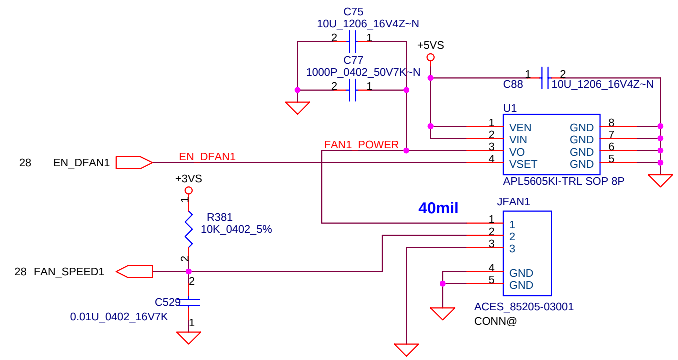
XDP Reserve



Thermal Sensor ADM1032ARMZ



Fan Control and Tachometer



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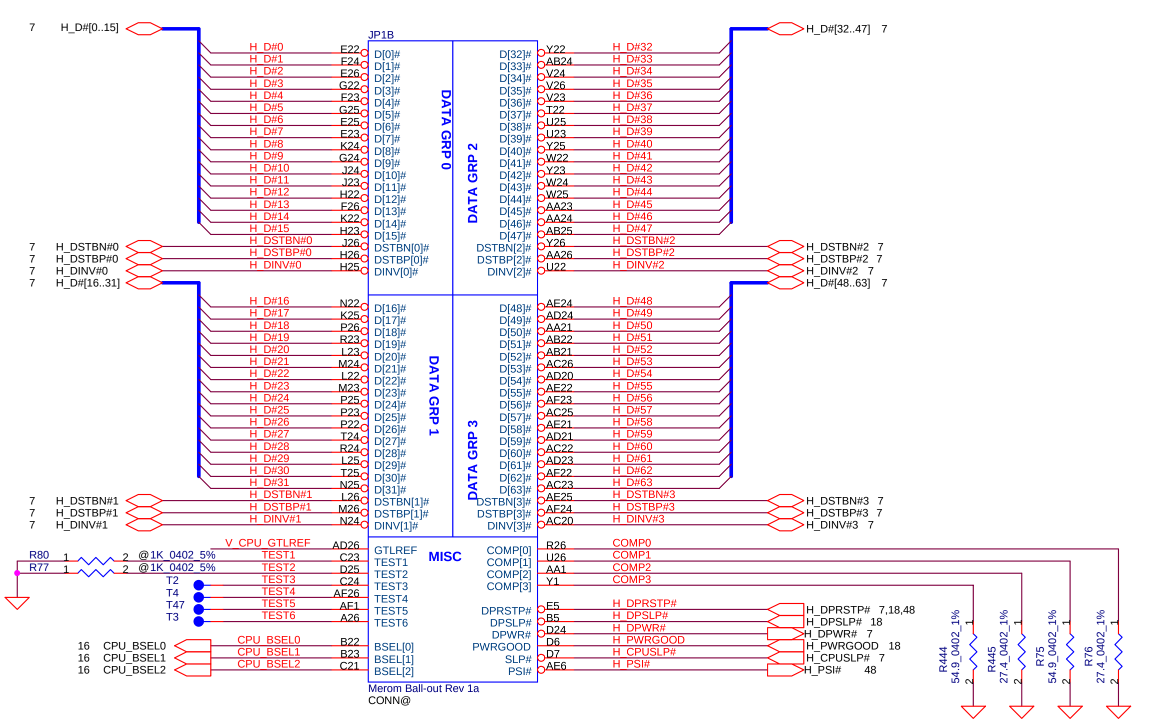
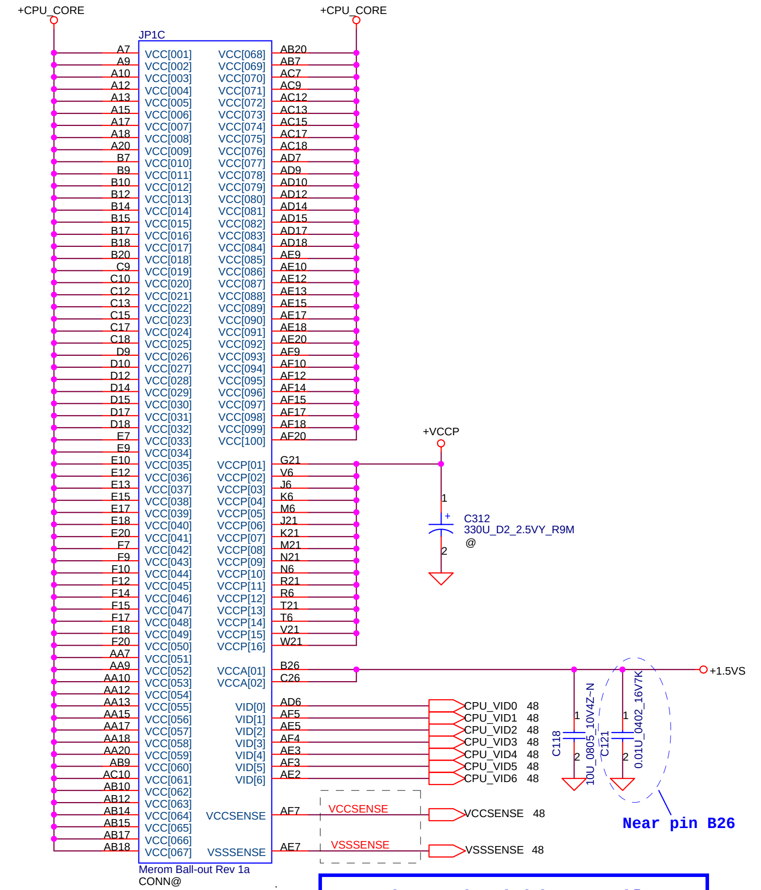
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: ***Merom(1/3)_AGTL+ / XDP***

Size	Document Number	Rev
Custom	LA-4131P	0.2



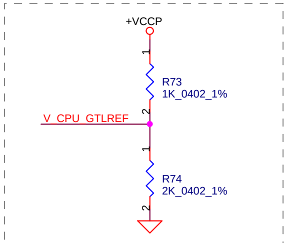


layout note: Route TEST3 & TEST5 traces on ground referenced layer to the TPs

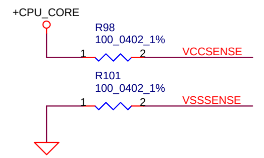
CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
166	0	1	1
200	0	1	0

Resistor placed within 0.5" of CPU pin.Trace should be at least 25 mils away from any other toggling signal. COMP[0,2] trace width is 18 mils. COMP[1,3] trace width is 4 mils.

Length match within 25 mils. The trace width/space/other is 20/7/25.



Close to CPU pin AD26 within 500mils.



Close to CPU pin within 500mils.

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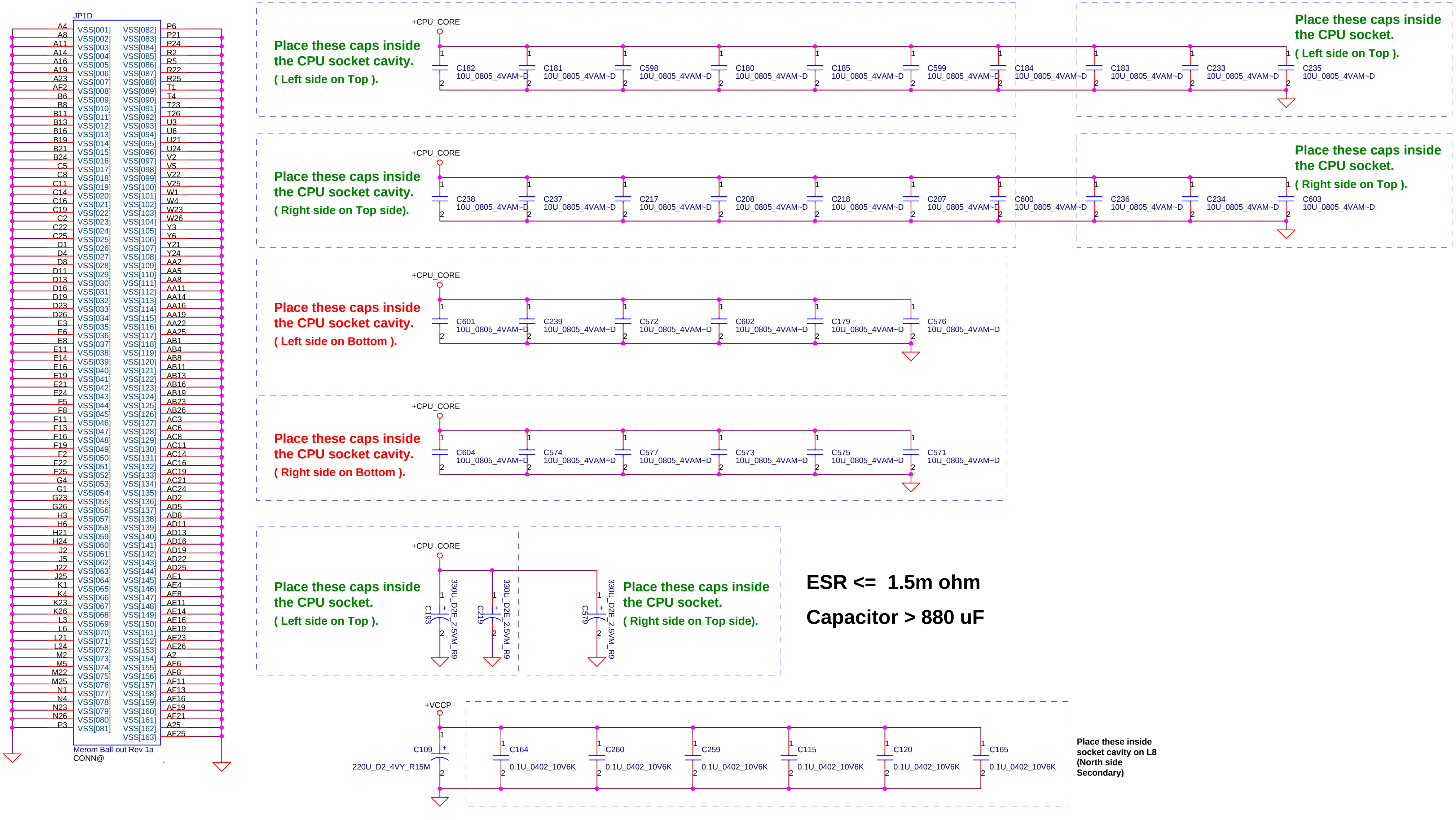
High Frequence Decoupling

10uF 0805 X5R -> 85 degree.



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Title	Merom(3/3)_GND / Bypass
Size: Custom	Document Number: LA-4131P
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Date:	Monday, February 18, 2008
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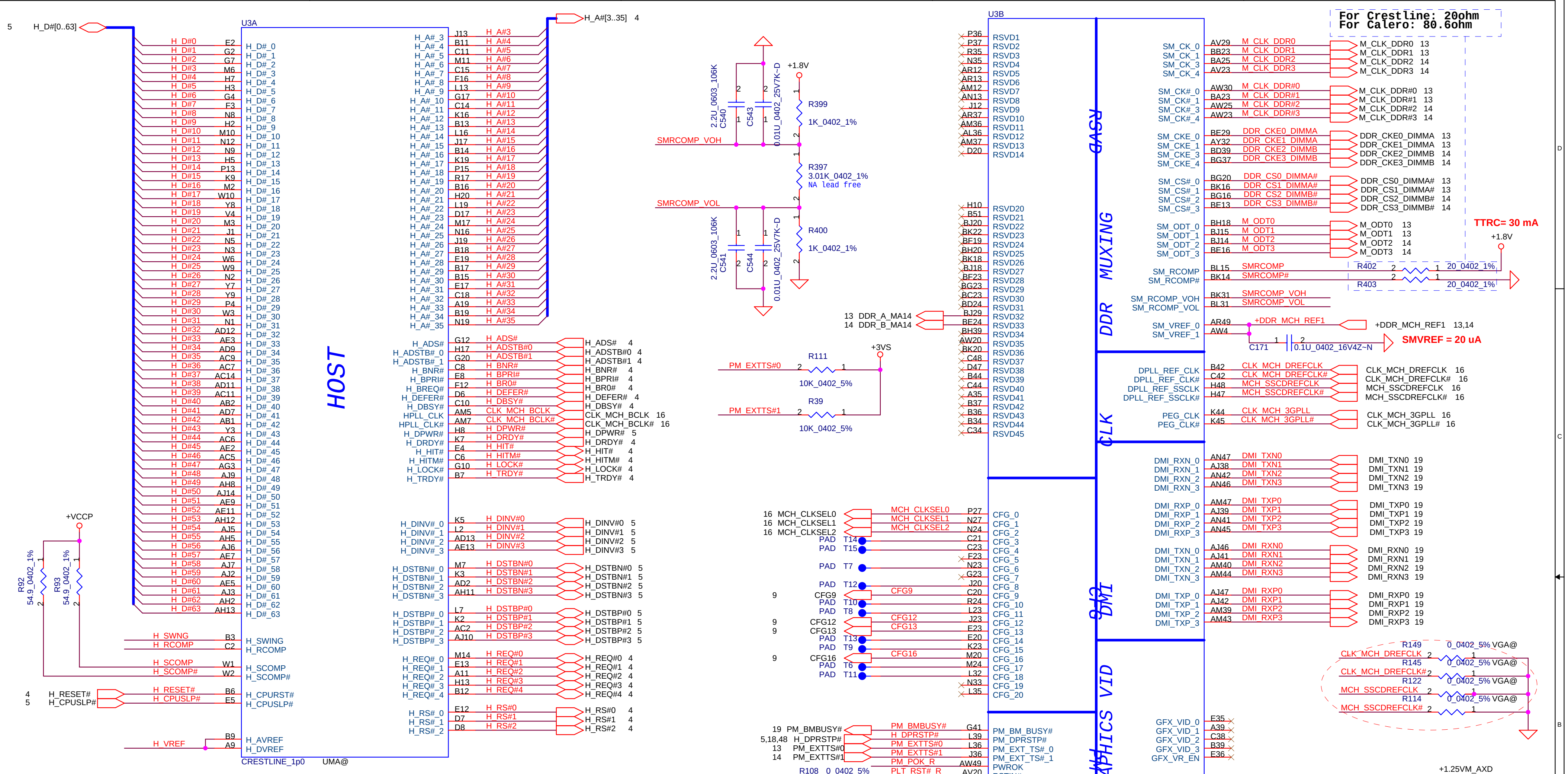
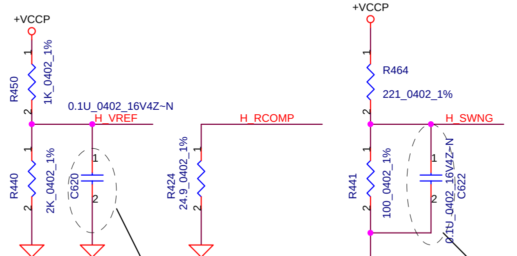


layout note:

Route H_SCOMP and H_SCOMP# with trace width, spacing and impedance (55 ohm) same as FSB data traces.

Layout Note:
H_RCOMP / H_VREF / H_SWNG
trace width and spacing is 10/20



within 100 mils from NB

Near B3 pin

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Title: **Crestline(1/6)_AGTL+/DDR2/DMI**

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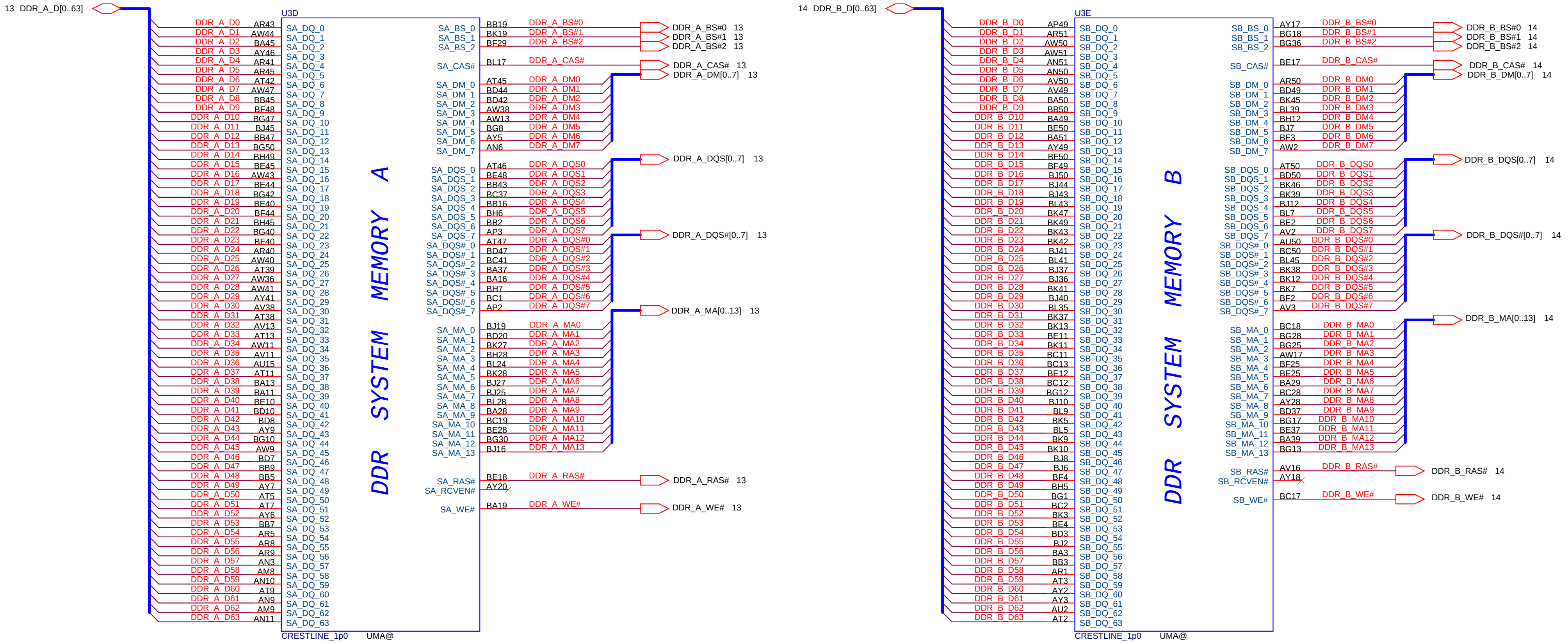

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Title: **Crestline(2/6)_DDR2 Channel A/B**

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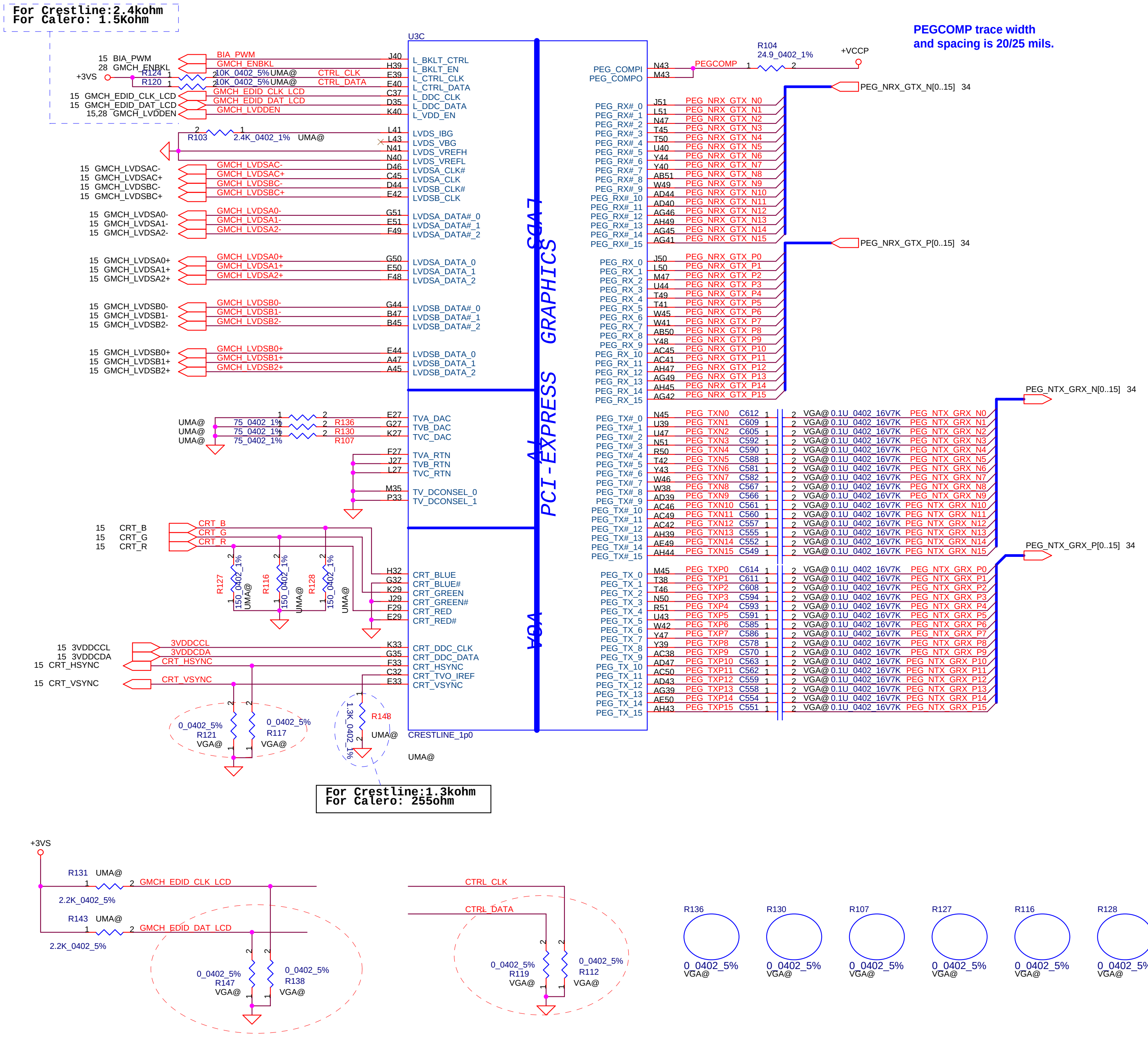
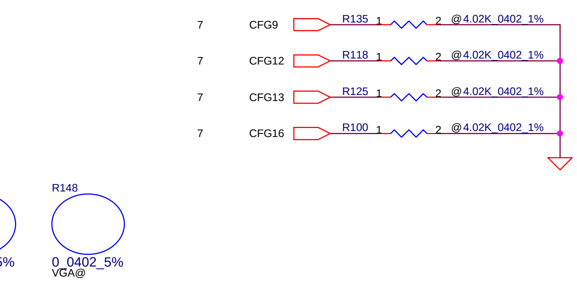
Date: Monday, February 18, 2008 | Sheet 8 of 53



Strap Pin Table

CFG[2:0] FSB Freq select	010 = FSB 800MHz 011 = FSB 667MHz Others = Reserved
CFG5 (DMI select)	0 = DMI x 2 1 = DMI x 4 *
CFG6	Reserved
CFG7 (CPU Strap)	0 = Reserved 1 = Mobile CPU *
CFG8 (Low power PCIE)	0 = Normal mode 1 = Low Power mode *
CFG9 (PCIE Graphics Lane Reversal)	0 = Reverse Lane 1 = Normal Operation *
CFG[11:10]	Reserved
CFG[13:12] (XOR/ALLZ)	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation(Default) *
CFG[15:14]	Reserved
CFG16 (FSB Dynamic ODT)	0 = Disabled 1 = Enabled *
CFG[18:17]	Reserved
SDVO_CTRLDATA	0 = No SDVO Device Present * 1 = SDVO Device Present
CFG19 (DMI Lane Reversal)	0 = Normal Operation * (Lane number in Order) 1 = Reverse Lane
CFG20 (PCIE/SDVO concurrent)	0 = Only PCIE or SDVO is operational. * 1 = PCIE/SDVO are operating simu.

CFG[17:3] have internal pull up
CFG[19:18] have internal pull down



Note: CRT / TV-out should route to JP30 first then to the JP1 & JP2 on system side.

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Title: **Crestline(3/6)_LVDS / CRT/ PCIE**

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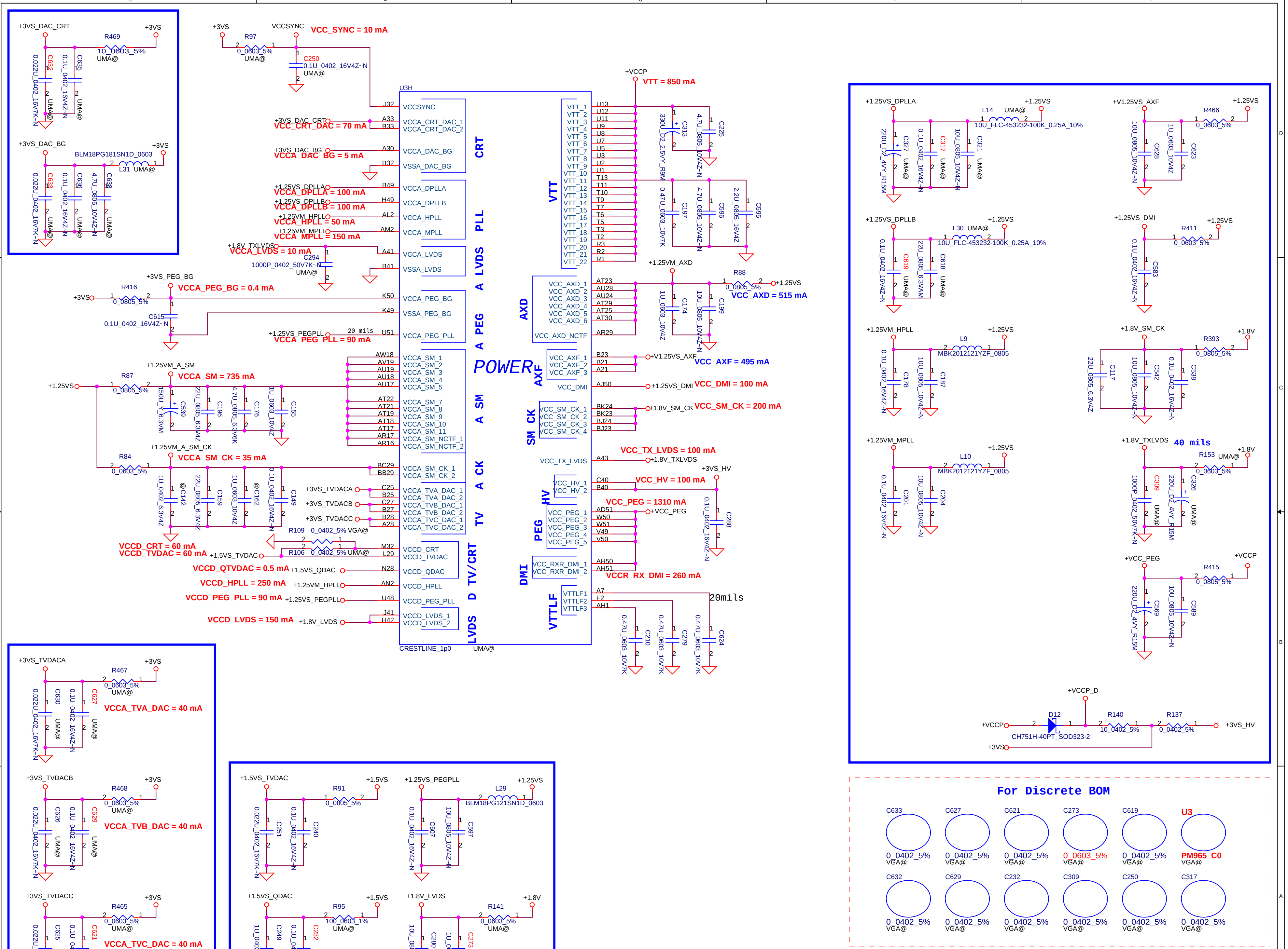

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Title: **Crestline(4/6)_PWR**

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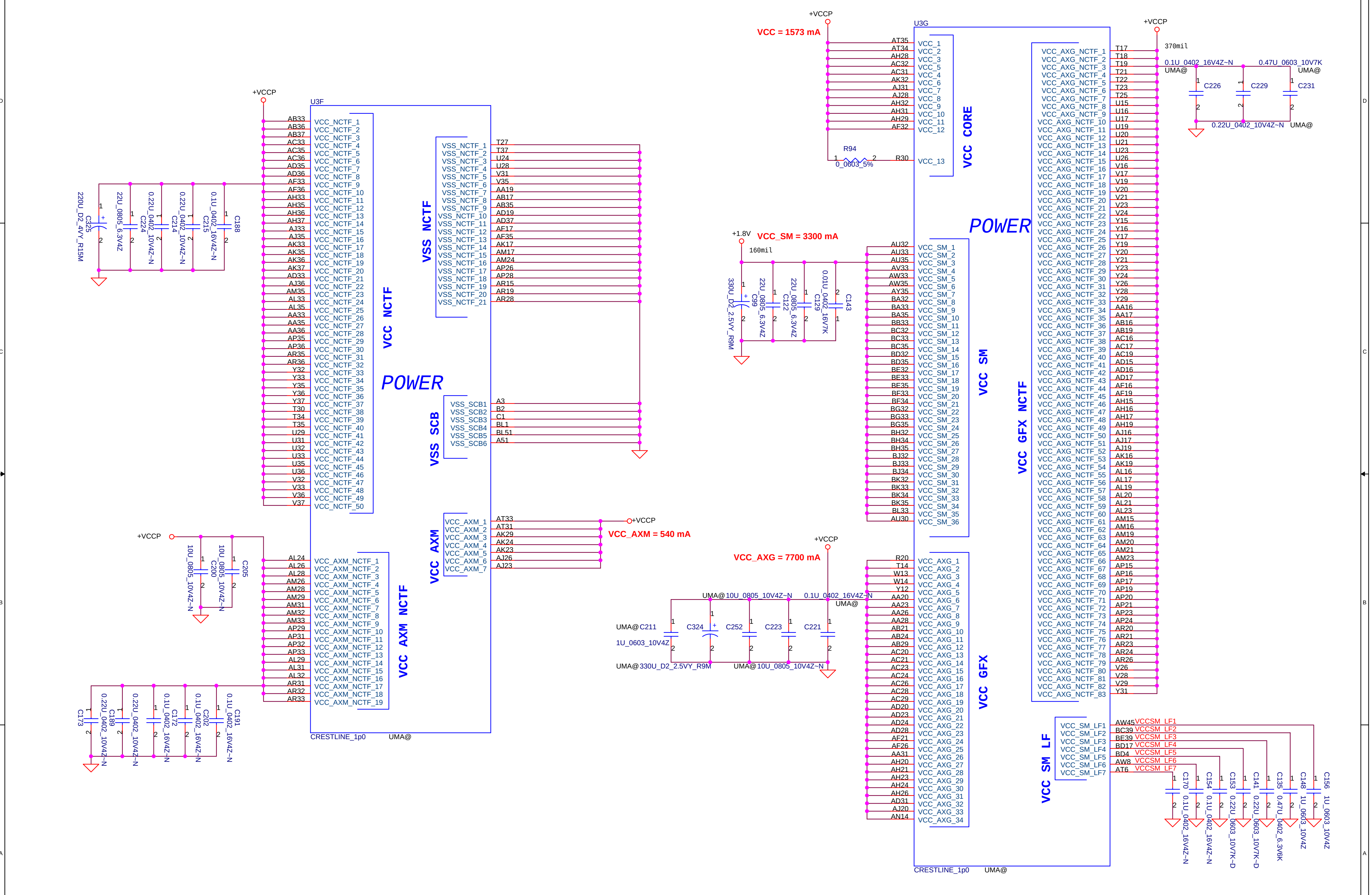

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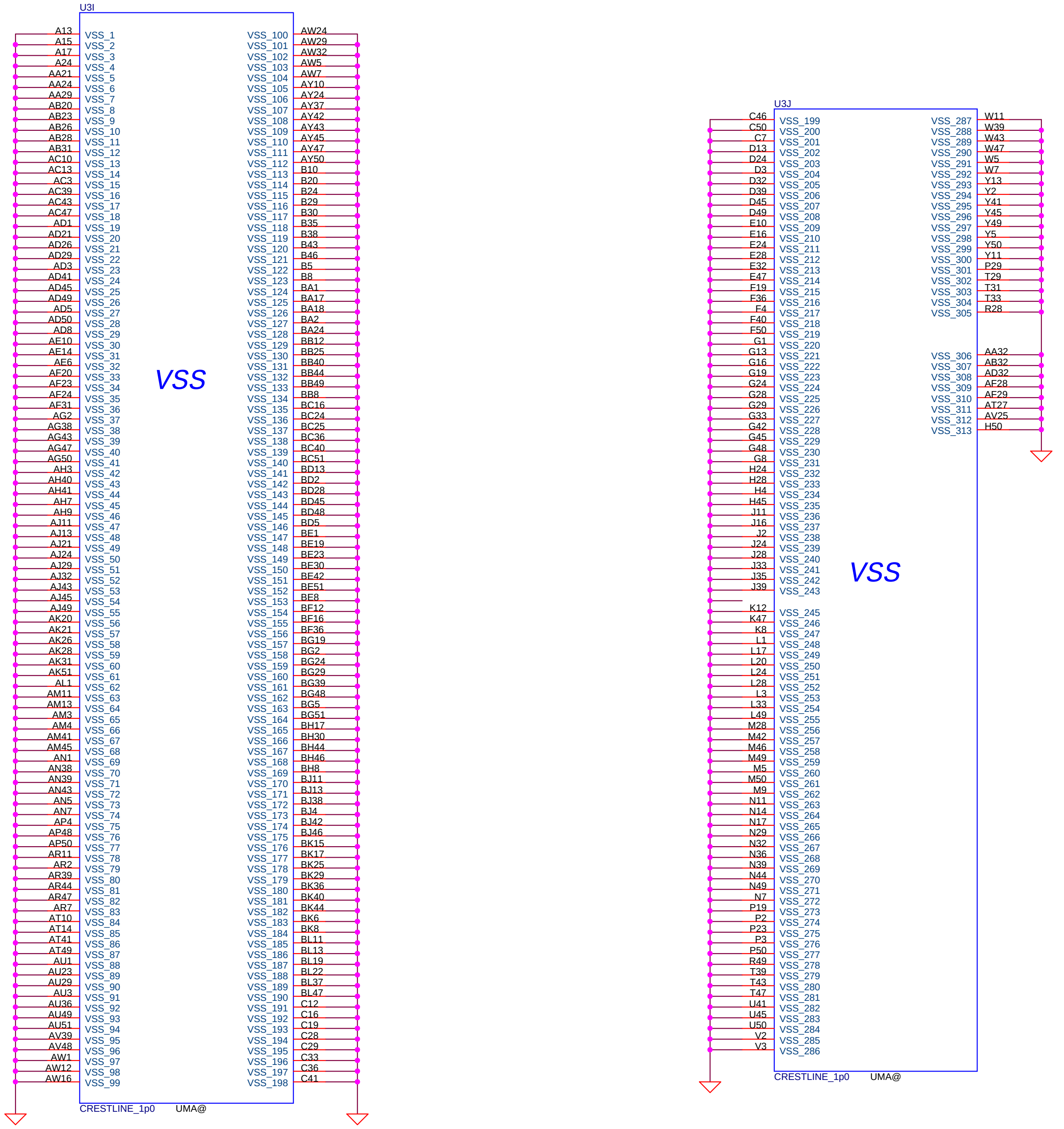
Issued Date	2007/12/28	Deciphered Date	2007/12/28	Title: Crestline(5/6)_PWR / GND

Size	Document Number	Rev
Custom	LA-4131P	0.2

Date: Monday, February 18, 2008



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Title: **Crestline(6/6)_GND**

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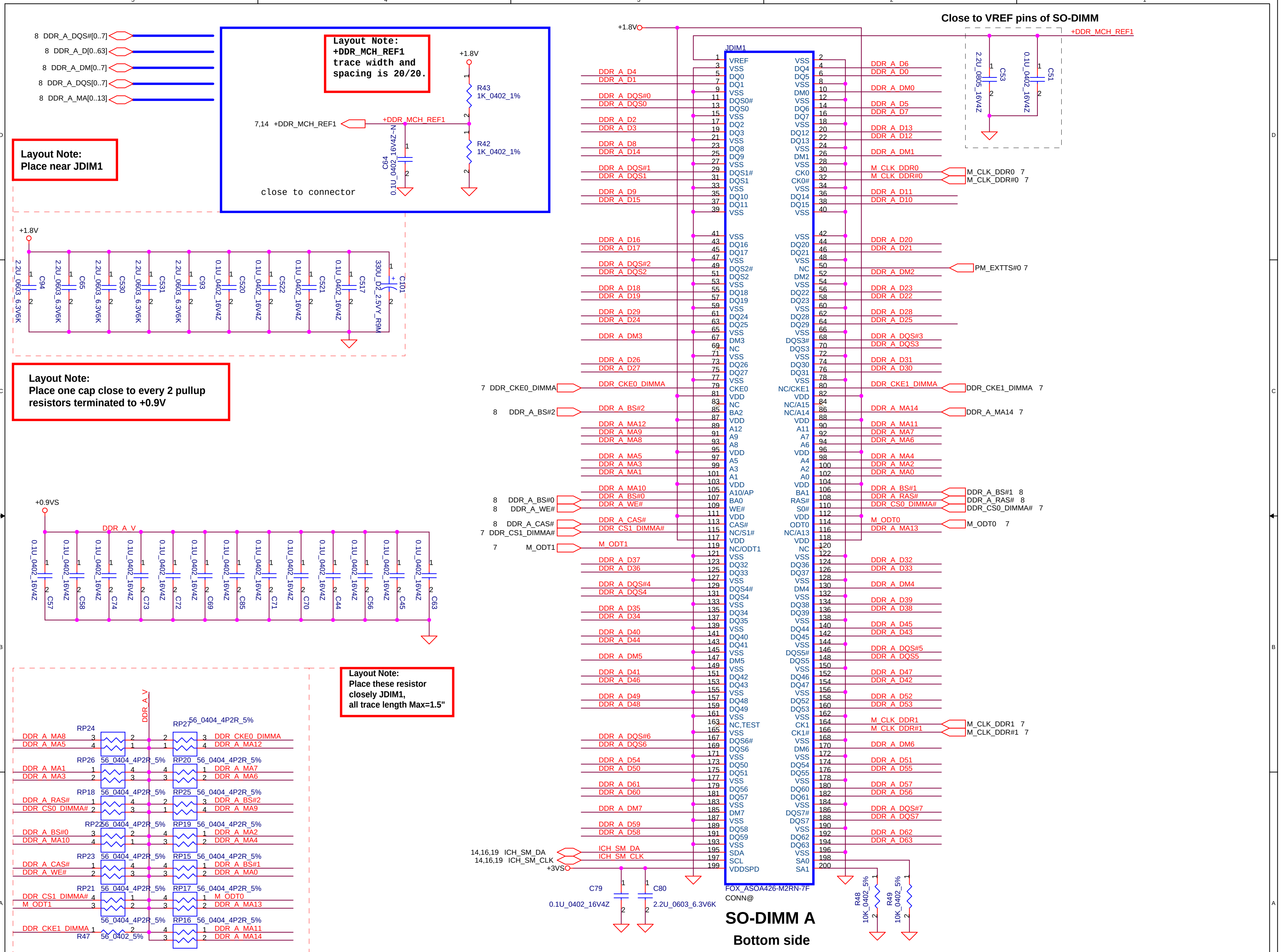

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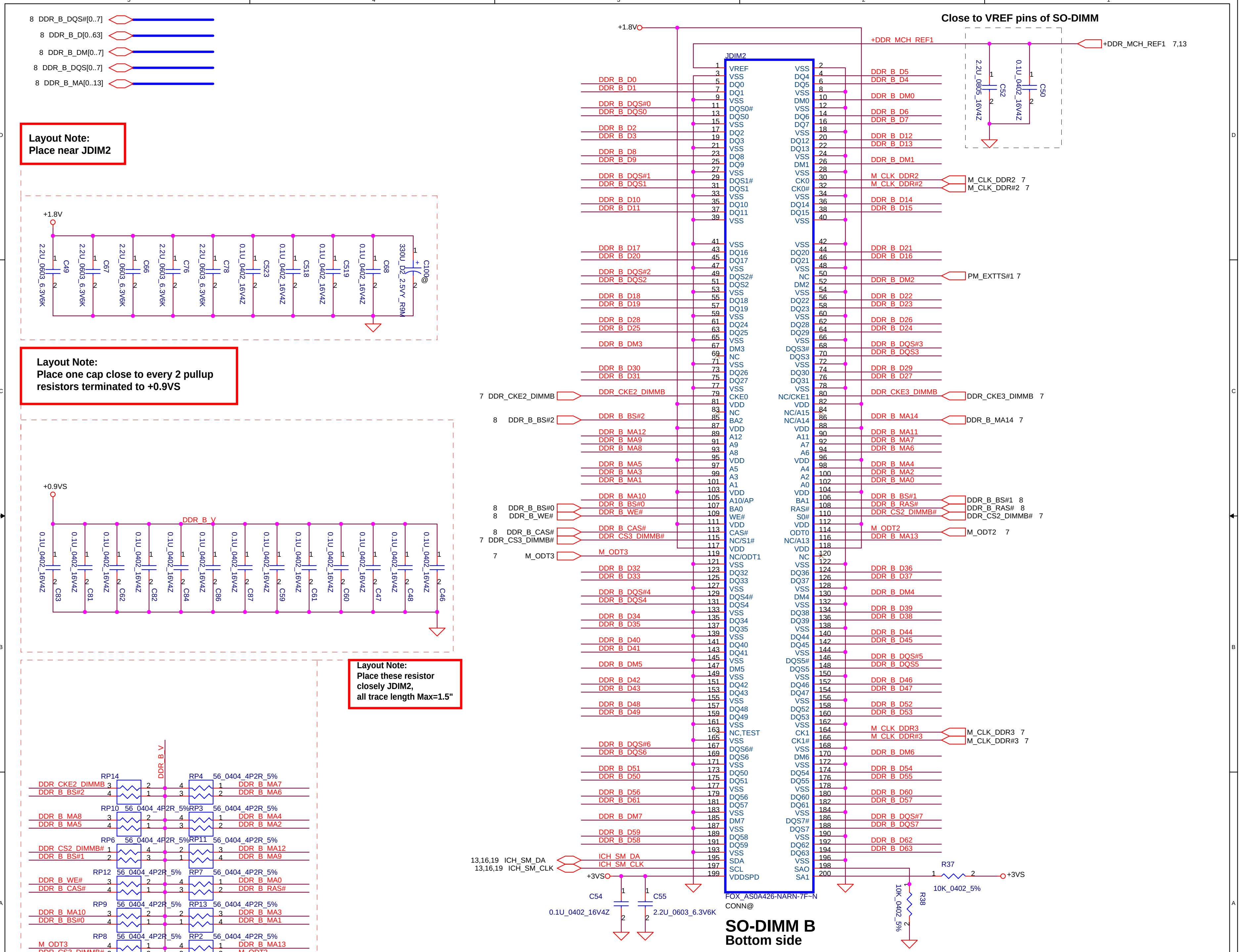

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Issued Date	2007/12/28	Deciphered Date	2007/12/28	Title: DDR2 SO-DIMM II

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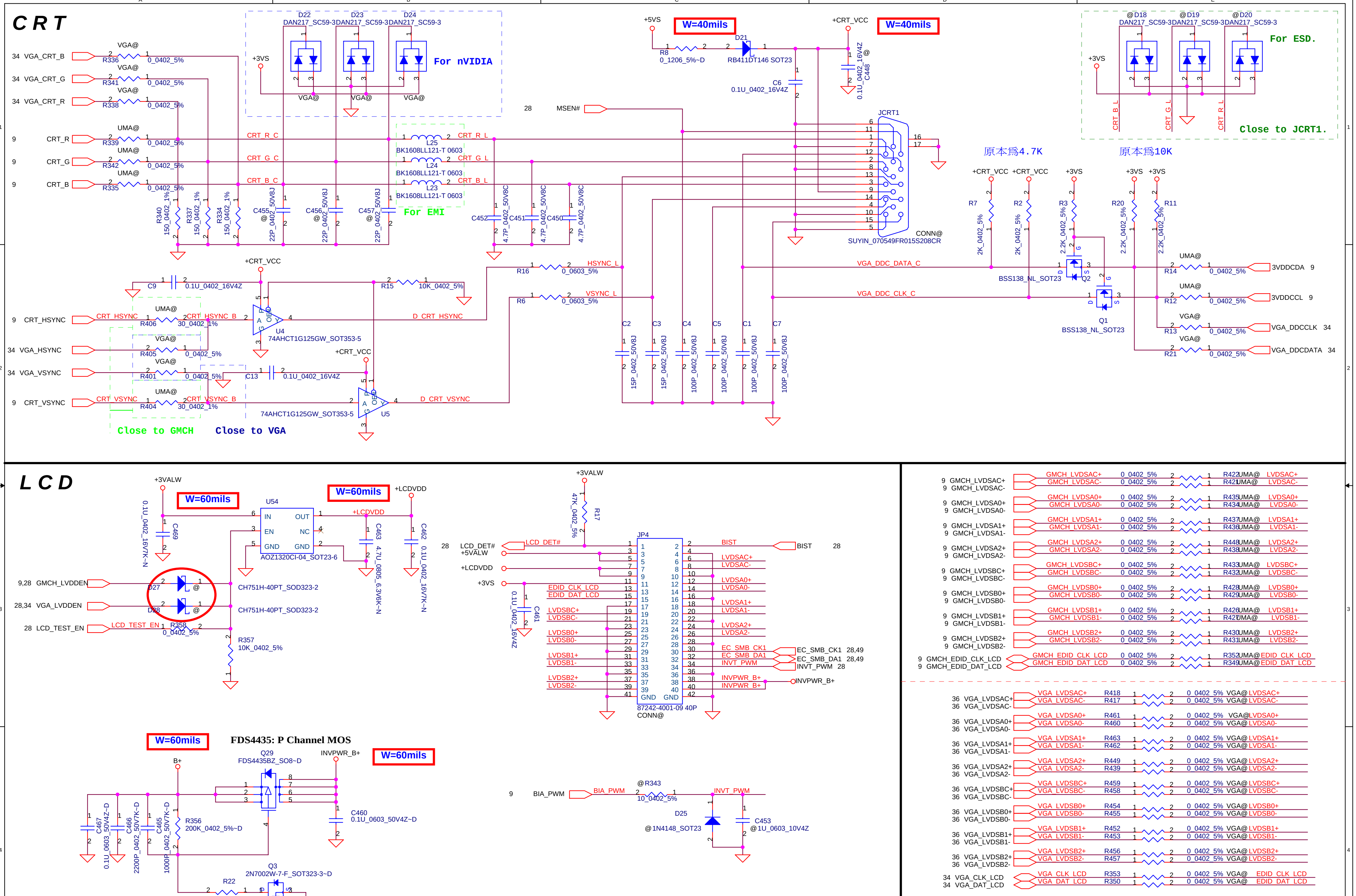
Date: Monday, February 18, 2008 | Sheet 14 of 53



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Title: **CRT Conn / LCD Conn**

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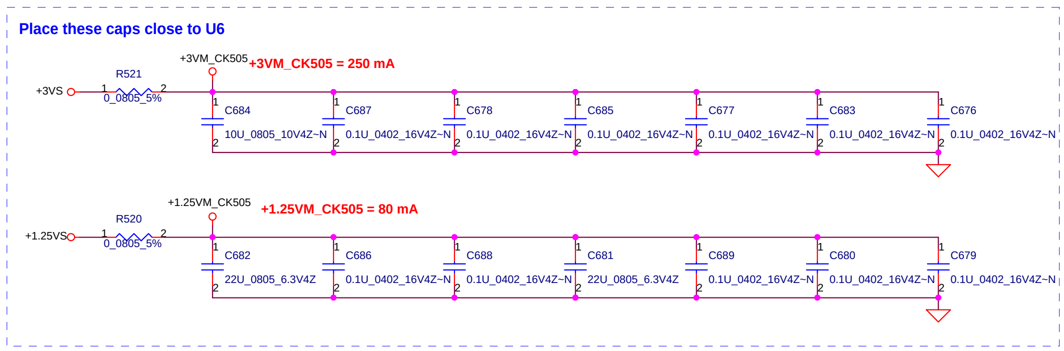
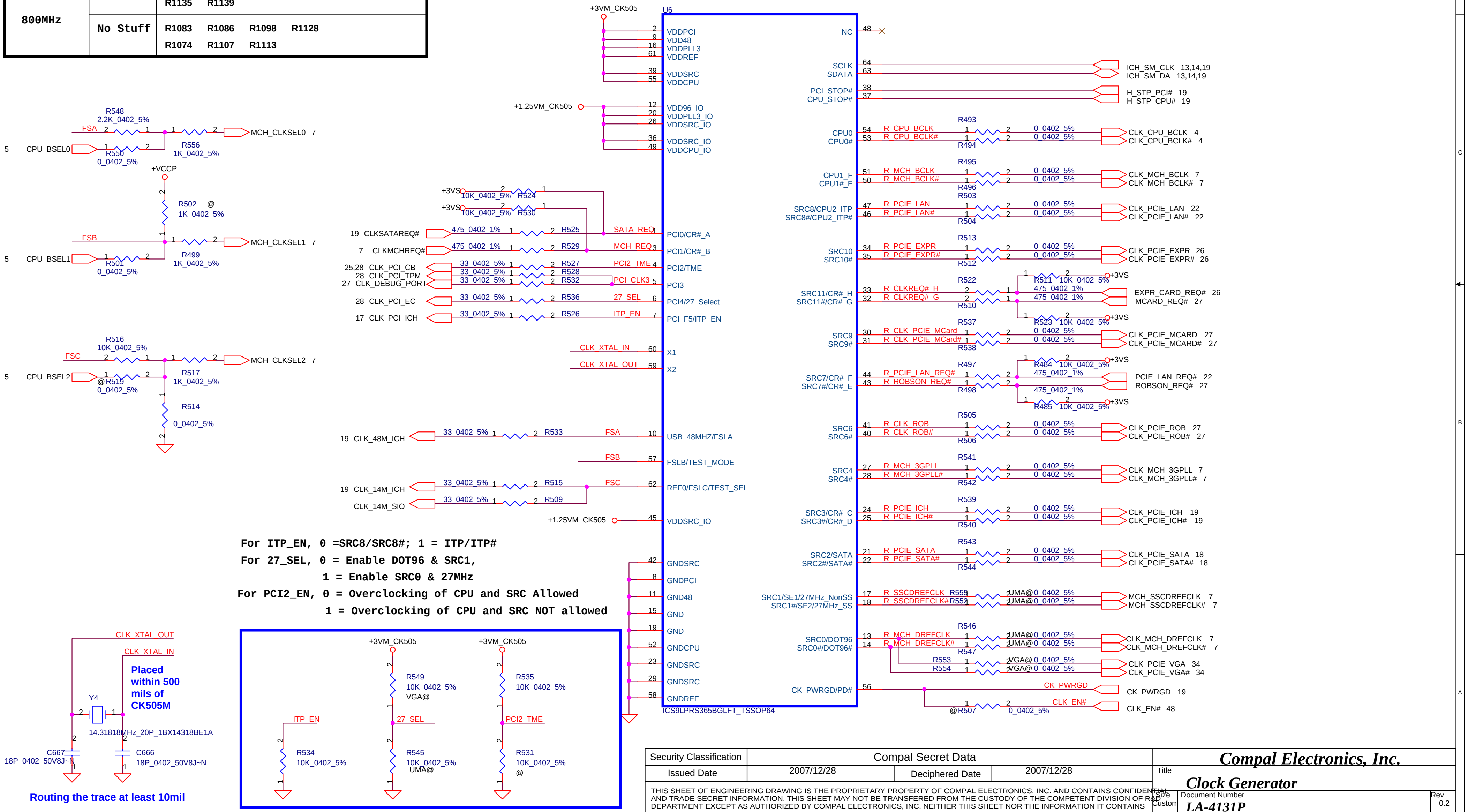
FSLC CLKSEL2	FSLB CLKSEL1	FSLA CLKSEL0	CPU MHz	SRC MHz	PCI MHz
0	1	0	200	100	33.3
0	1	1	166	100	33.3

FSB Frequency Selet:

CPU Driven	Stuff	R1107 R1135 R1083
*(Default)	No Stuff	R1074 R1086 R1098 R1113 R1128 R1139
667MHz	Stuff	R1086 R1139 R1135 R1074 R1139 R1135
	No Stuff	R1083 R1107 R1128 R1113 R1098
800MHz	Stuff	R1135 R1139
	No Stuff	R1083 R1086 R1098 R1128 R1074 R1107 R1113





For ITP_EN, 0 =SRC8/SRC8#; 1 = ITP/ITP#
For 27_SEL, 0 = Enable DOT96 & SRC1,
1 = Enable SRC0 & 27MHz
For PCI2_EN, 0 = Overclocking of CPU and SRC Allowed
1 = Overclocking of CPU and SRC NOT allowed

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Compal Electronics, Inc.

Title: **Clock Generator**

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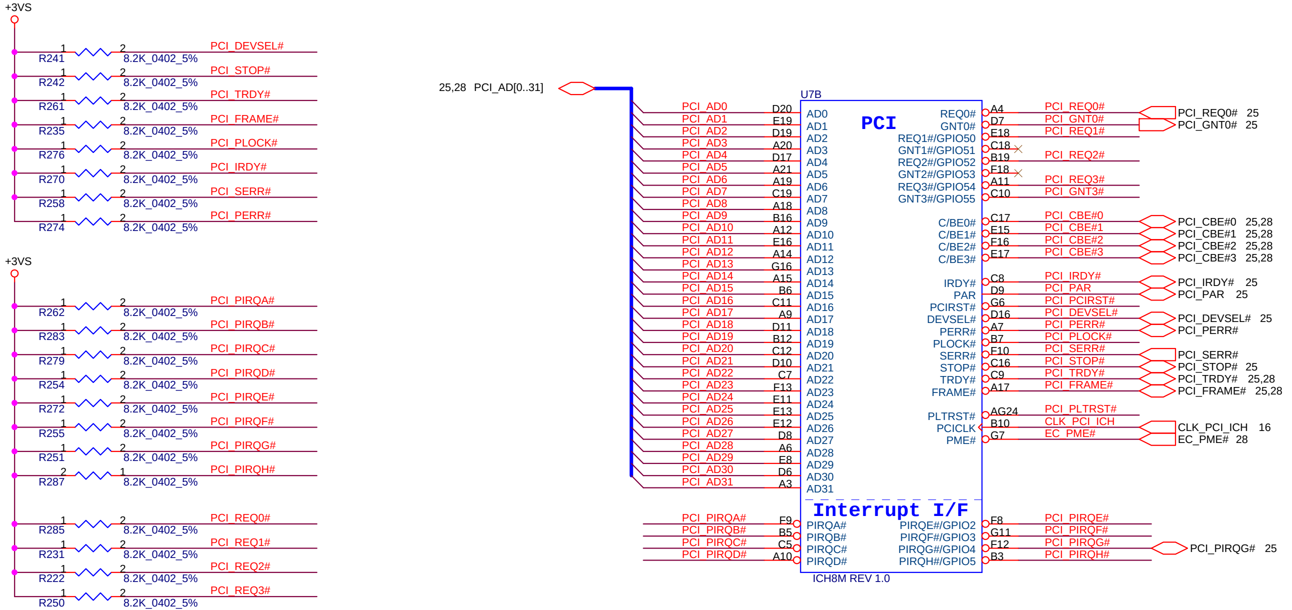
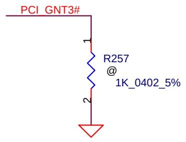
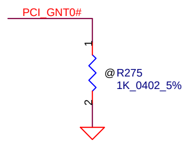




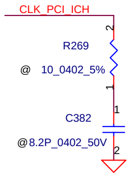
Check if use LPC

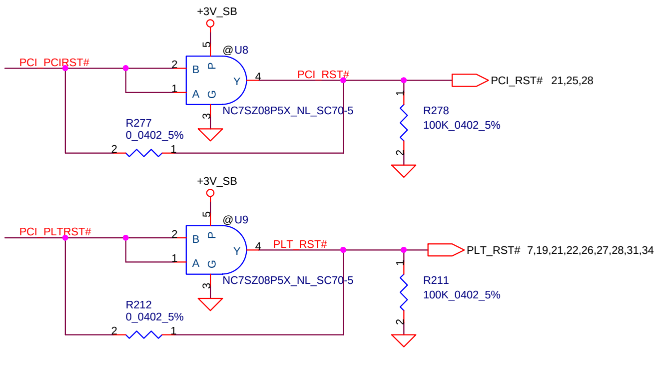
Boot BIOS Strap		
PCI_GNT0#	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC *



A16 swap override Strap	
PCI_GNT3#	Low= A16 swap override Enble High= Default *

Place closely pin B10





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Title: **ICH8M(1/4)_PCI / INT**

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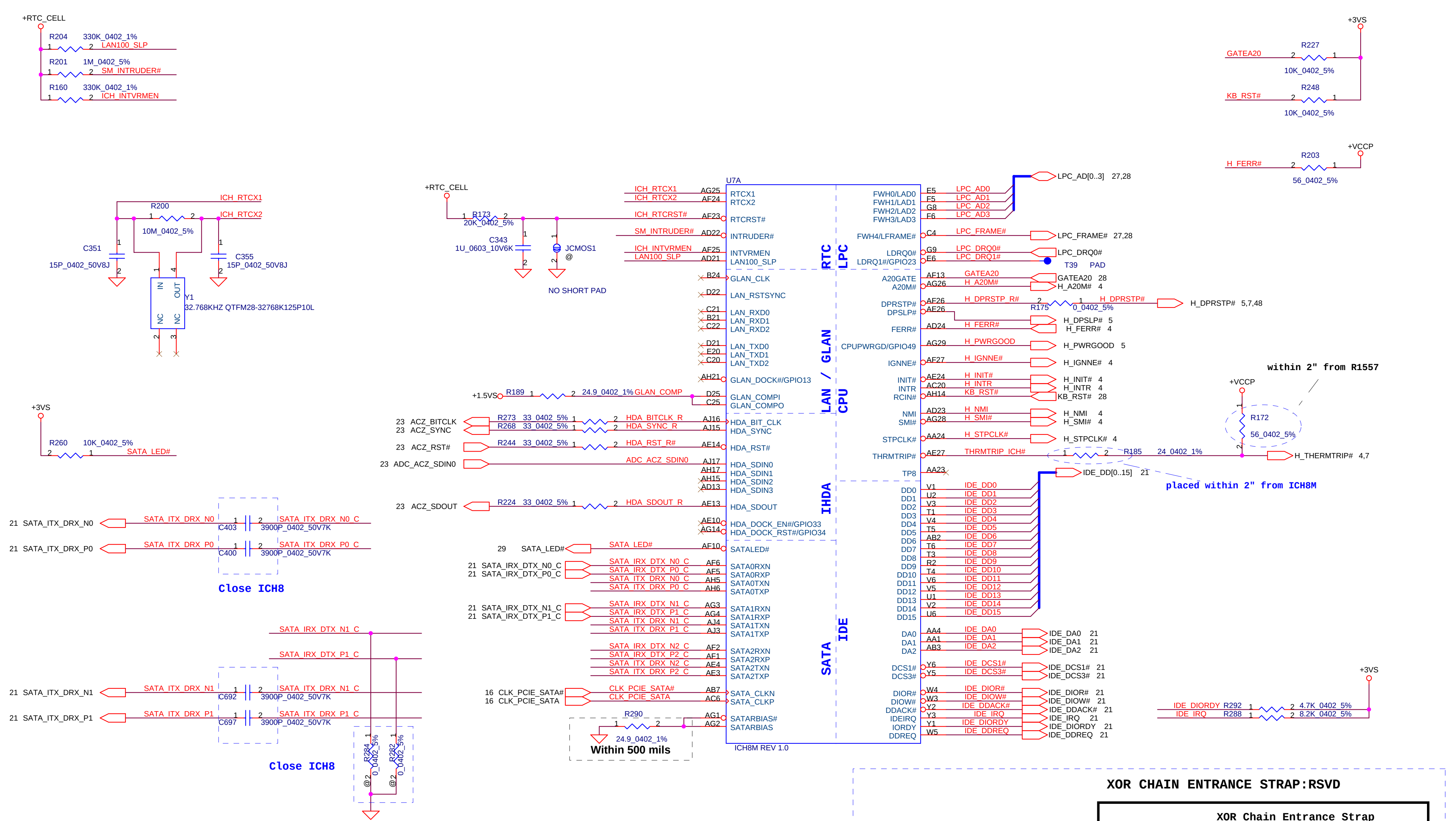

XOR CHAIN ENTRANCE STRAP:RSVD

XOR Chain Entrance Strap		
ICH RSVD	HDA SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal Operation (Default)
1	1	Set PCIE port config bit 1

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Title: **ICH8M(2/4)_LAN / HD / IDE / LPC**

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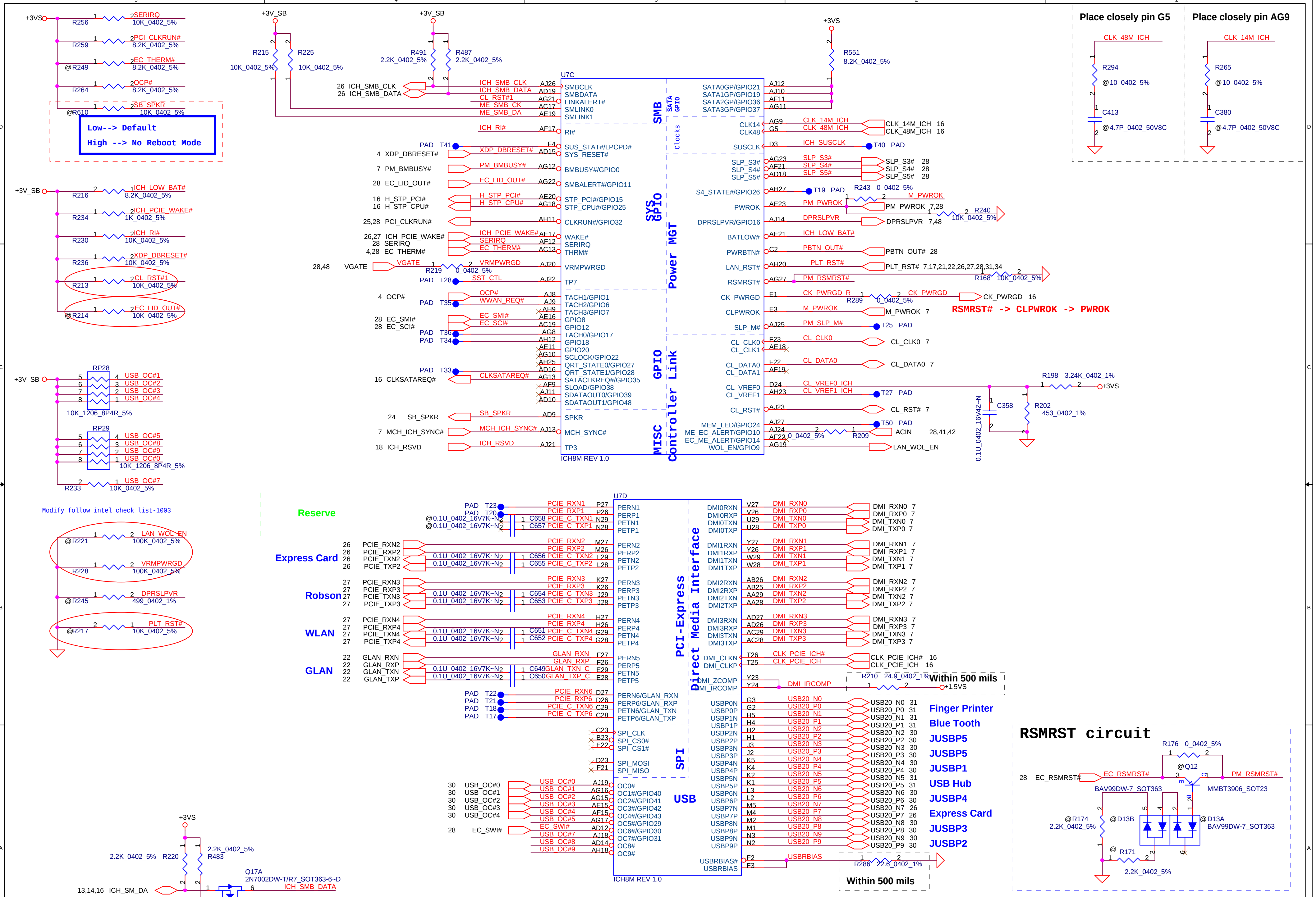

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Title: ICH8M(3/4)_PM/USB/GPIO

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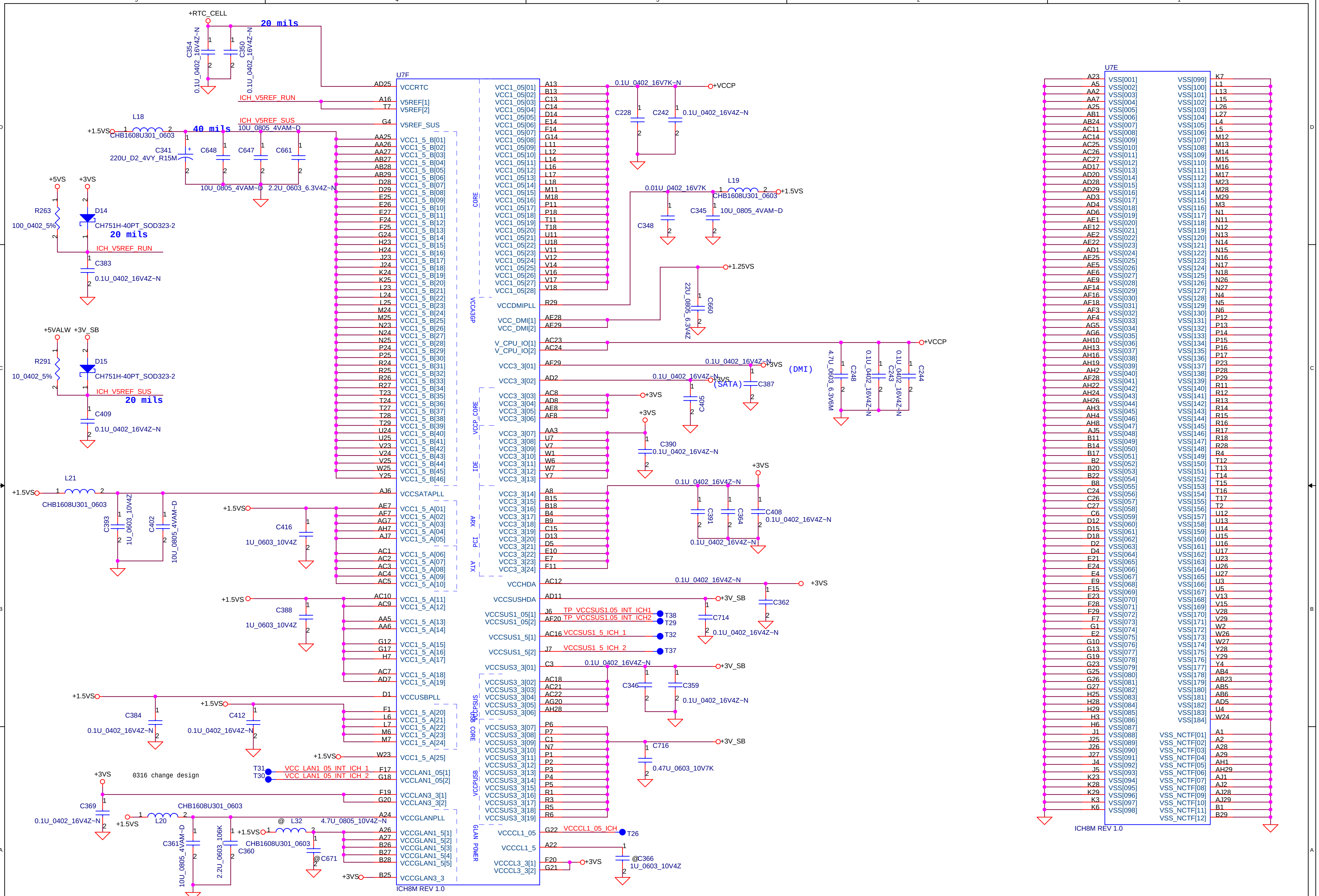
Date: Monday, February 18, 2008 |



Security Classification	Compal Secret Data	
Issued Date	2007/12/28	Deciphered Date 2007/12/28

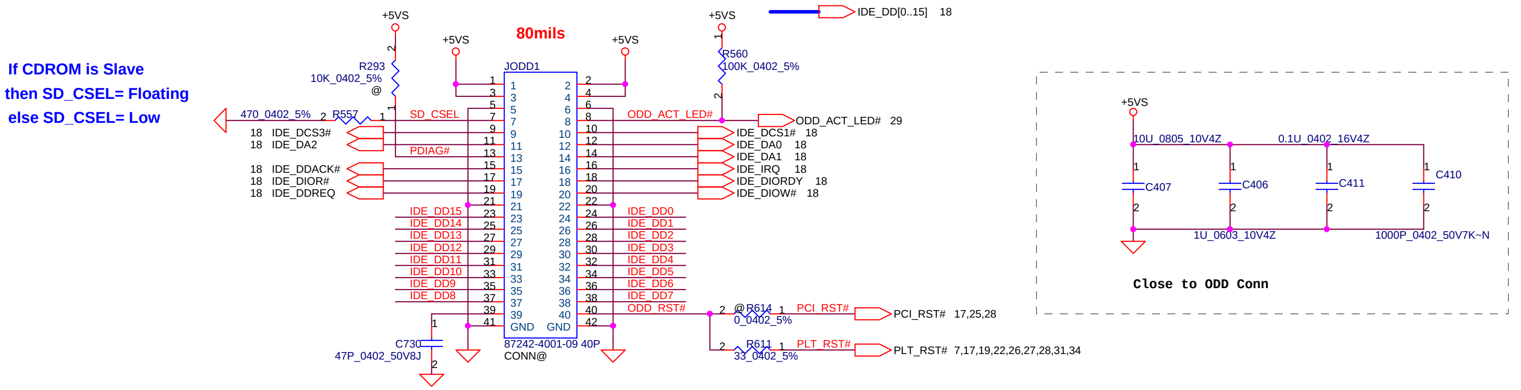
Compal Electronics, Inc.

Title: *ICH8M(4/4)_Power / GND*

Size: Custom | Document Number: *LA-4131P* | Rev: 0.2

Date: Monday, February 18, 2008 |

ODD Conn



SATA HDD1 Conn

SATA HDD2 Conn

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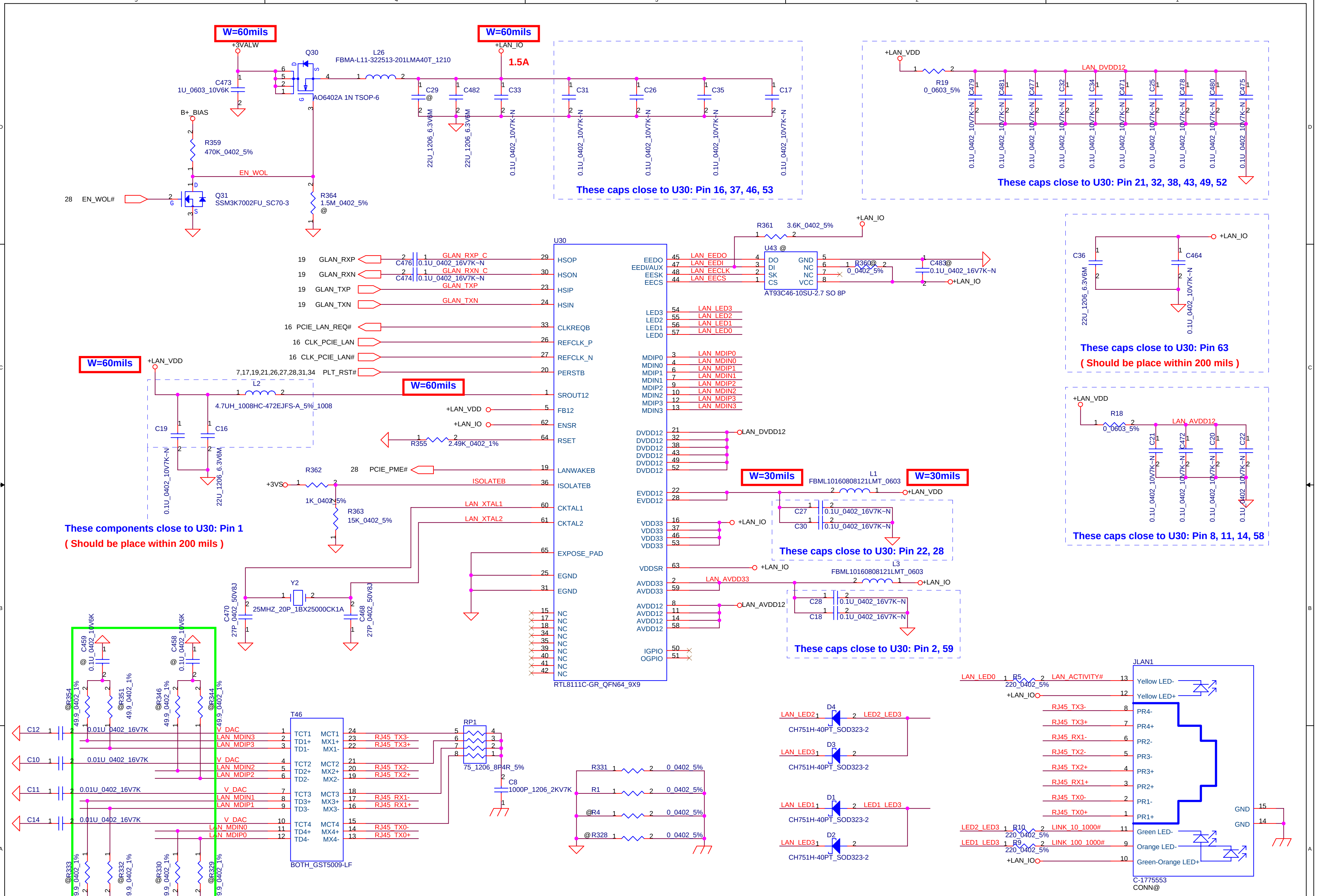

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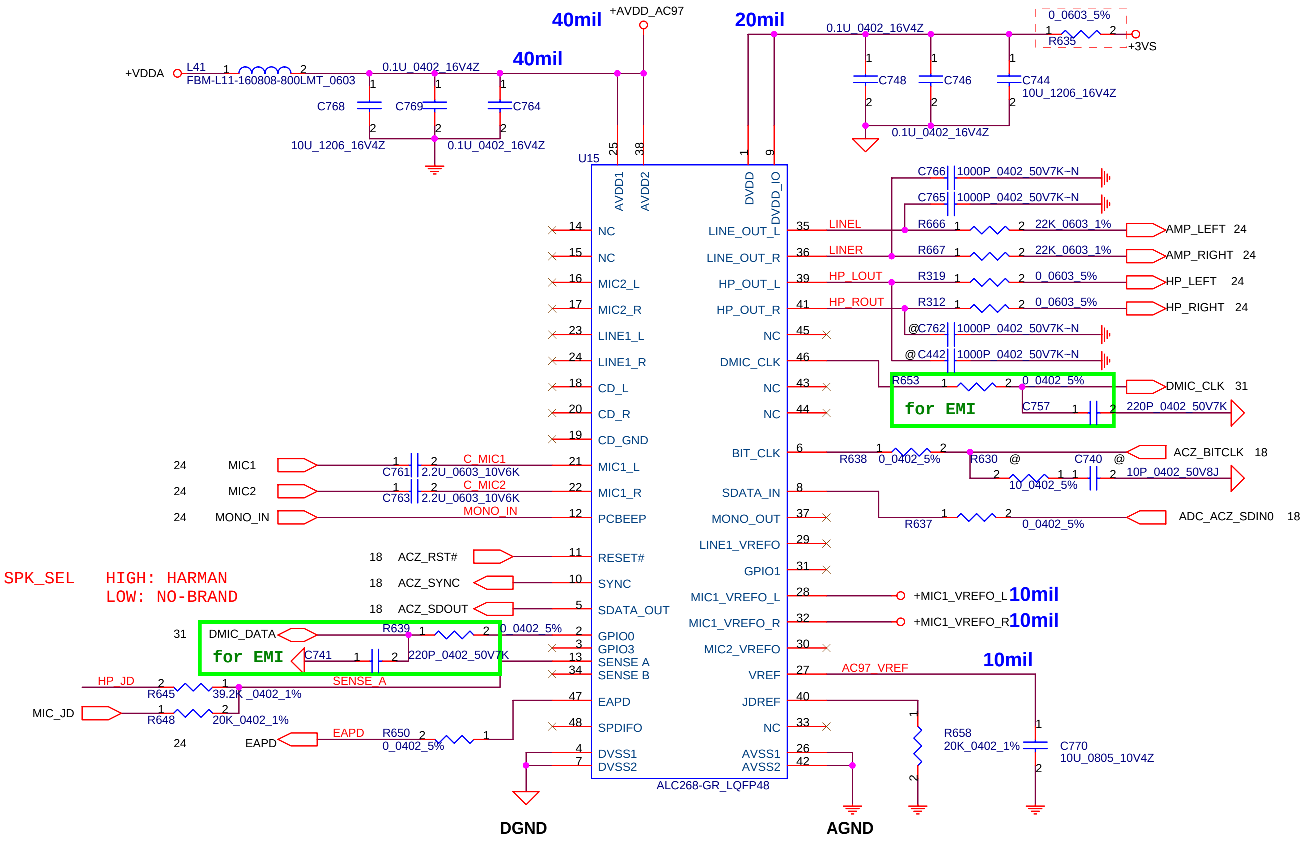
Compal Electronics, Inc.

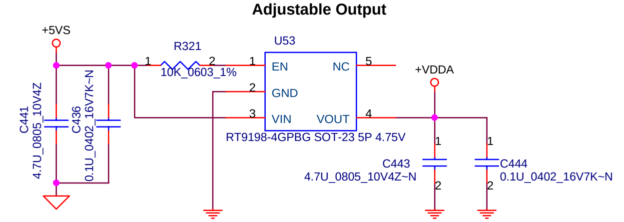
Title: **Realtek RTL8111C-GR**

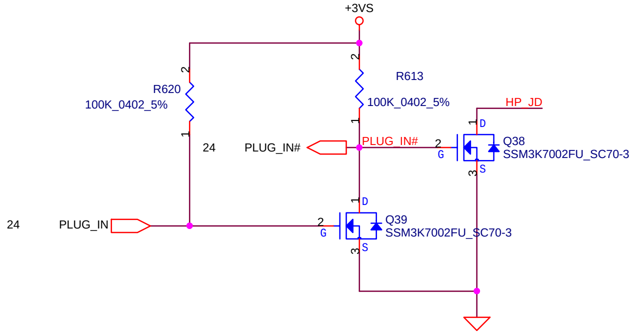
Size: Custom | Document Number: **LA-4131P** | Rev 0.2

HD Audio Codec



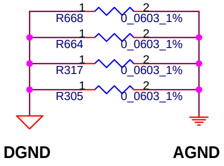
Regulator for Codec





Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 39, 41)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 35, 36)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 43, 44)
	5.1K	PORT-H (PIN 45, 46)

Moat Bridge



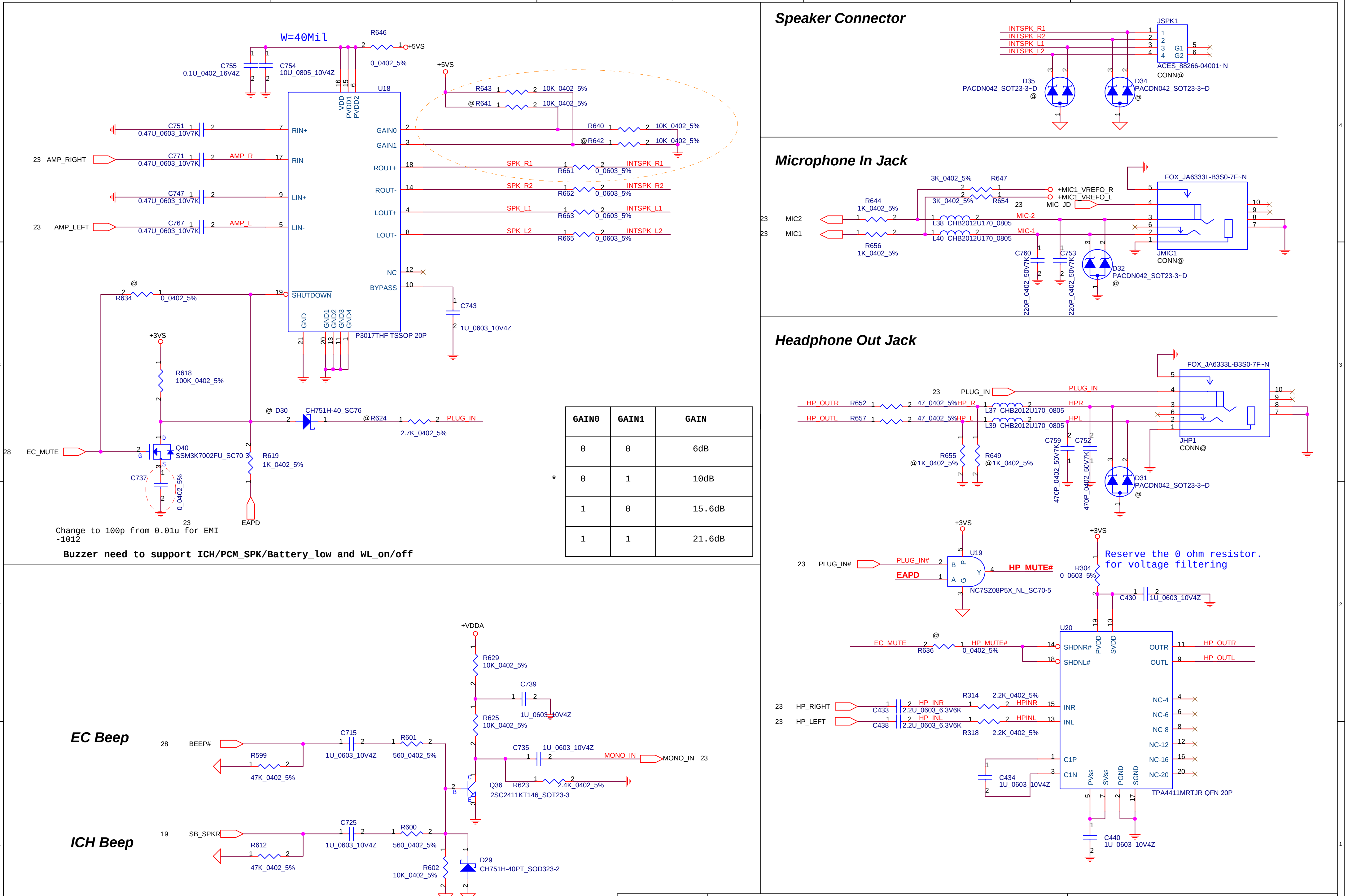
Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: *Audio Codec_ALC268*

Size: Custom | Document Number: *LA-4131P* | Rev: 0.2

Date: Monday, February 18, 2008 |



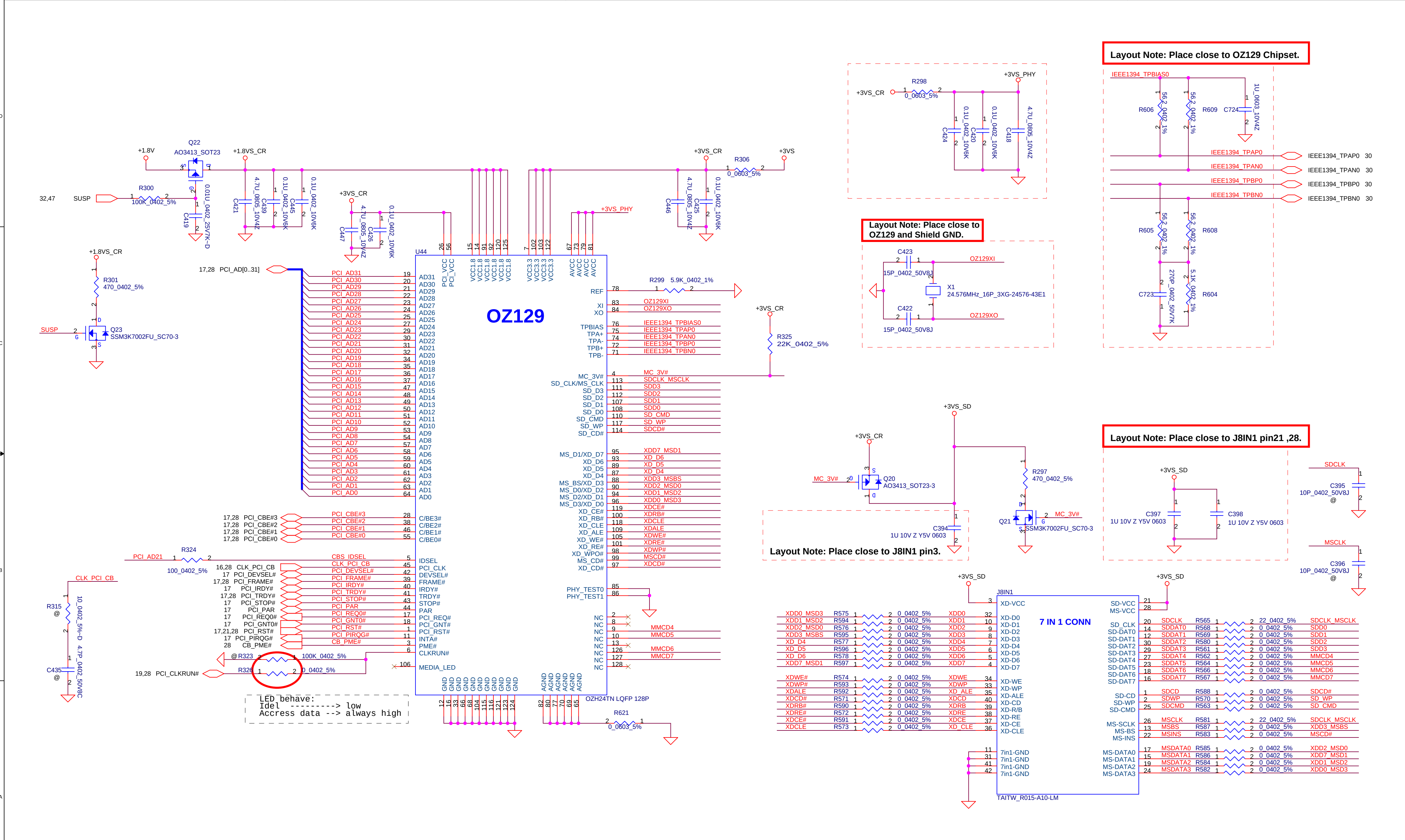
GAIN0	GAIN1	GAIN
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: **AMP / Audio Jack**

Size: Custom | Document Number: LA-4131P | Rev 0.2



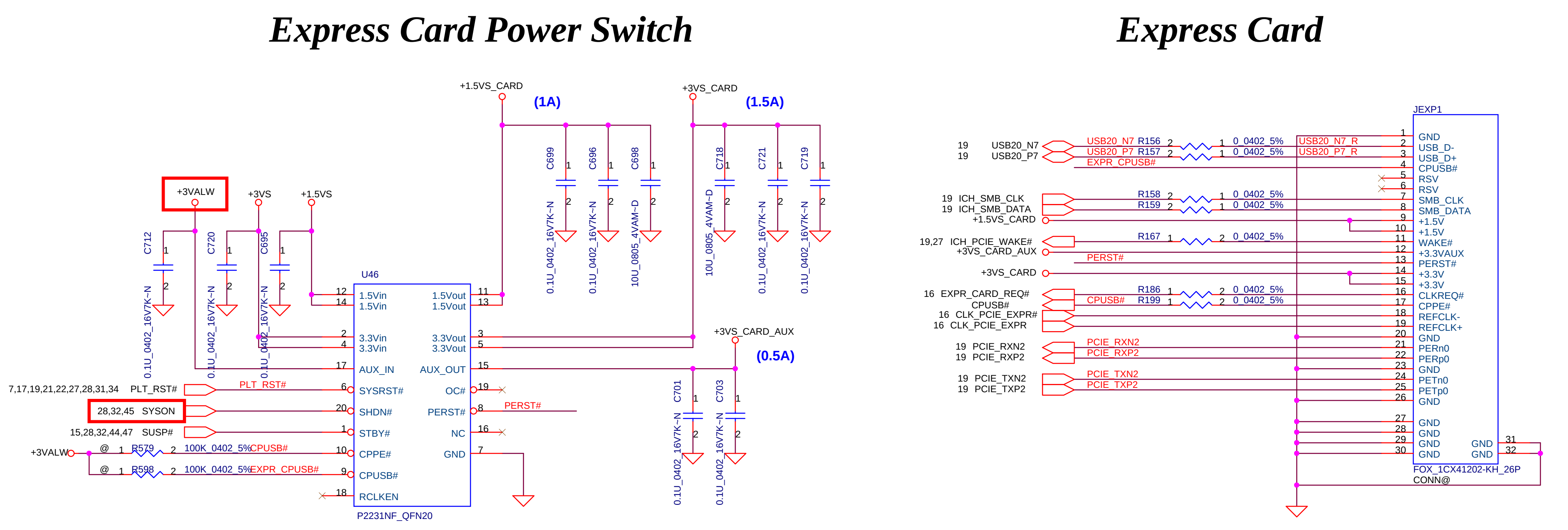
Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: **OZH24_Card Reader / 1394**

Size	Document Number	Rev
Custom	LA-4131P	0.2

Date: Monday, February 18, 2008 |



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/12/28	Deciphered Date	2007/12/28	Title: Express Card
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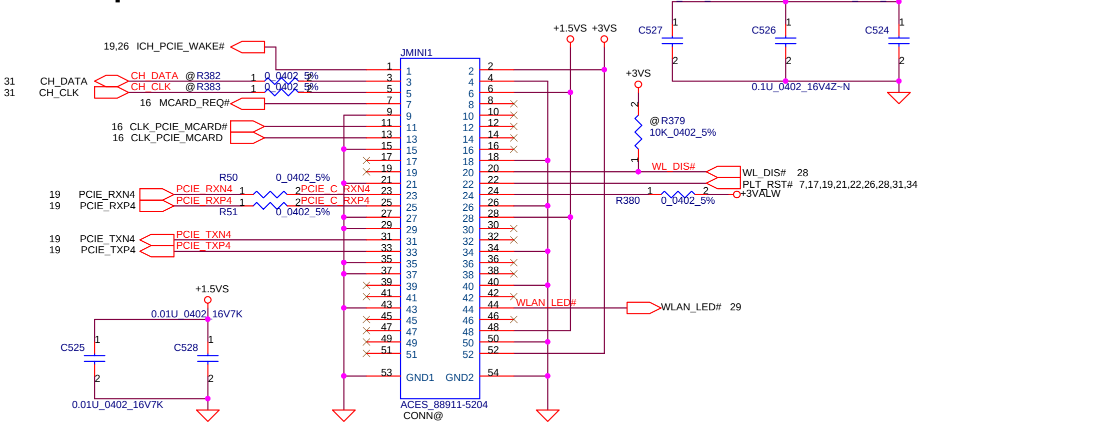
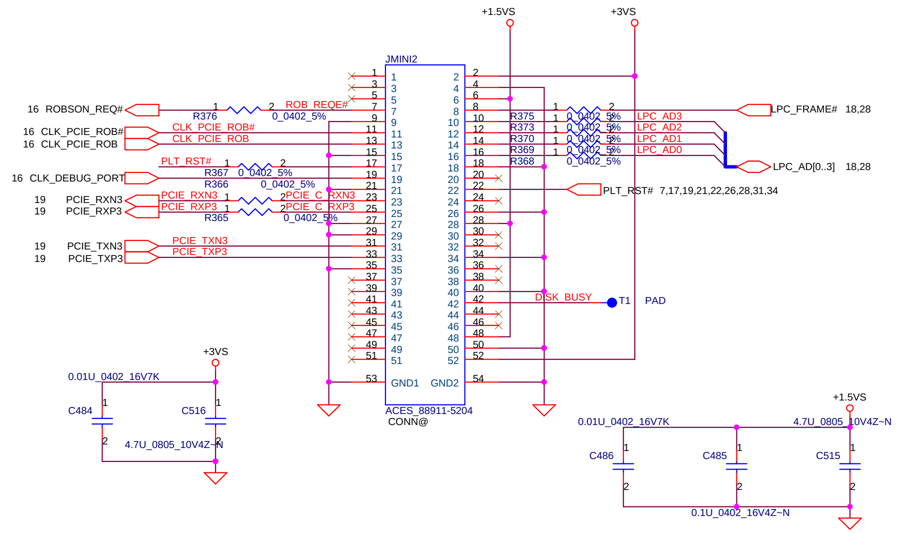
				Date: Monday, February 18, 2008; Sheet 26 of 53

Mini-Express Card---WLAN



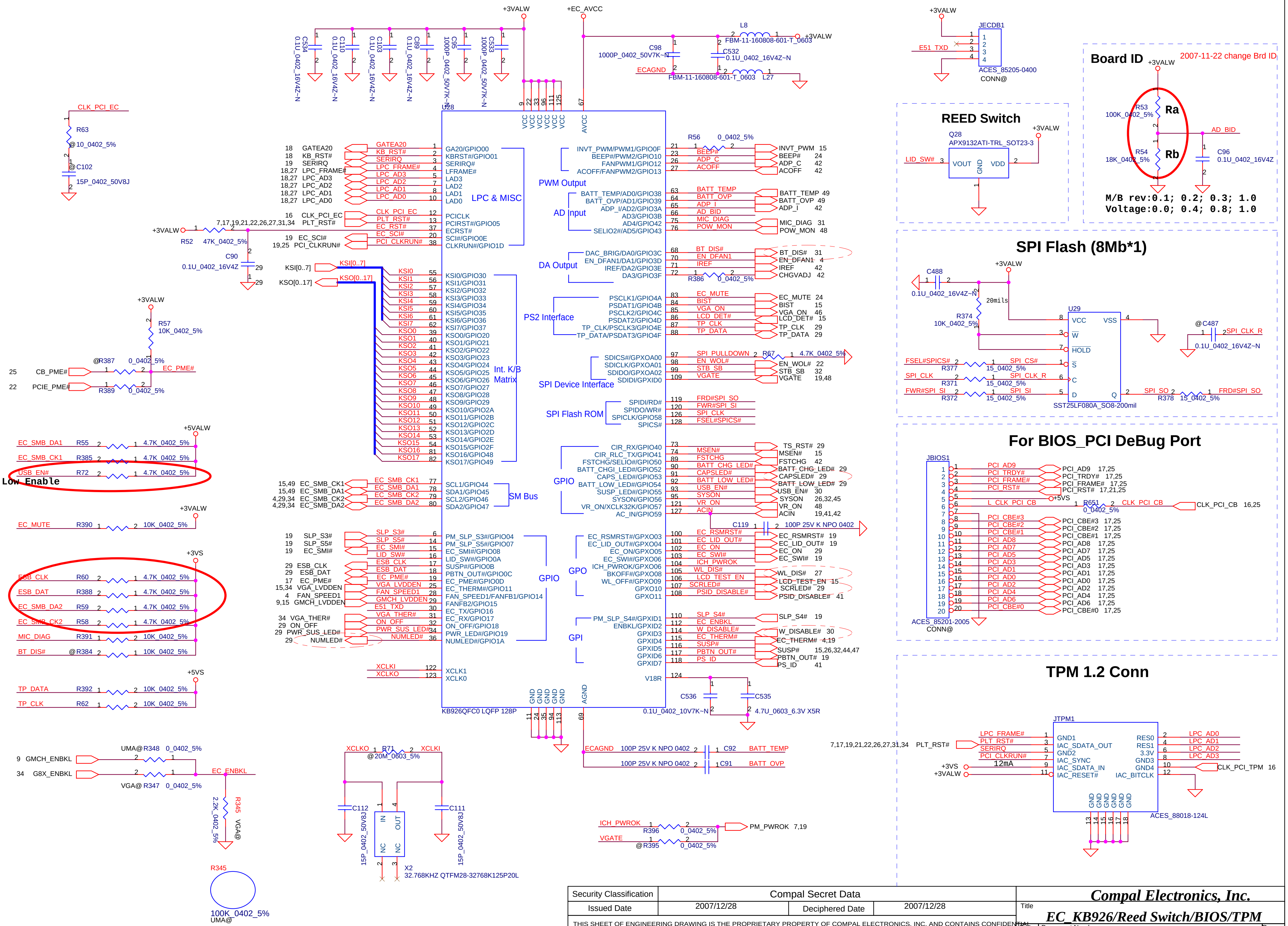
Mini-Express Card---Robson



Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title	Mini Card	
Size: Custom	Document Number: LA-4131P	Rev: 0.2
Date: Monday, February 18, 2008	Sheet 27 of 53	



Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

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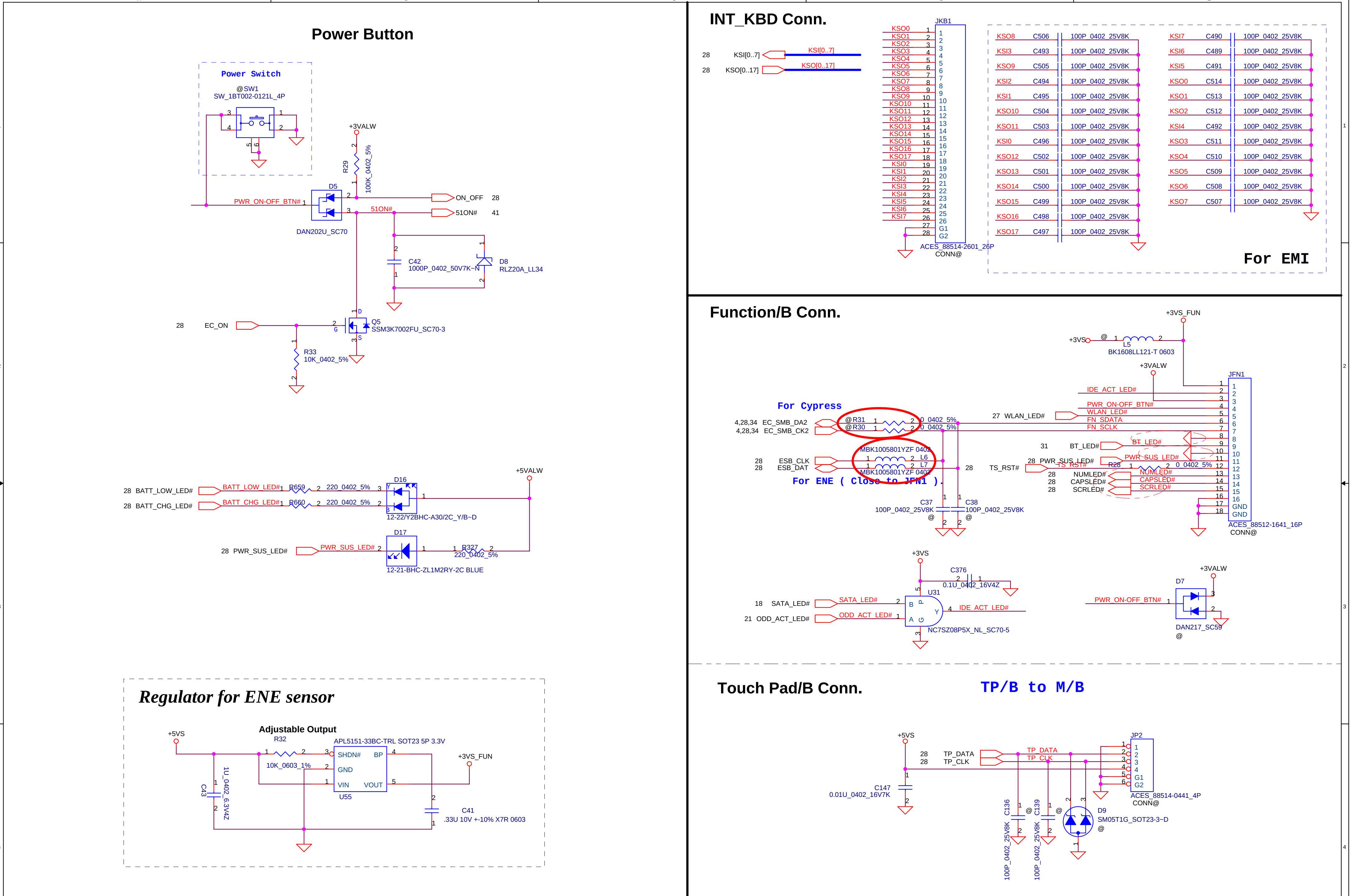
Compal Electronics, Inc.

Title
EC_KB926/Reed Switch/BIOS/TPM

Size	Document Number	Rev
Custom	LA-4131P	0.2

Date: Monday, February 18, 2008 Sheet 28 of 53

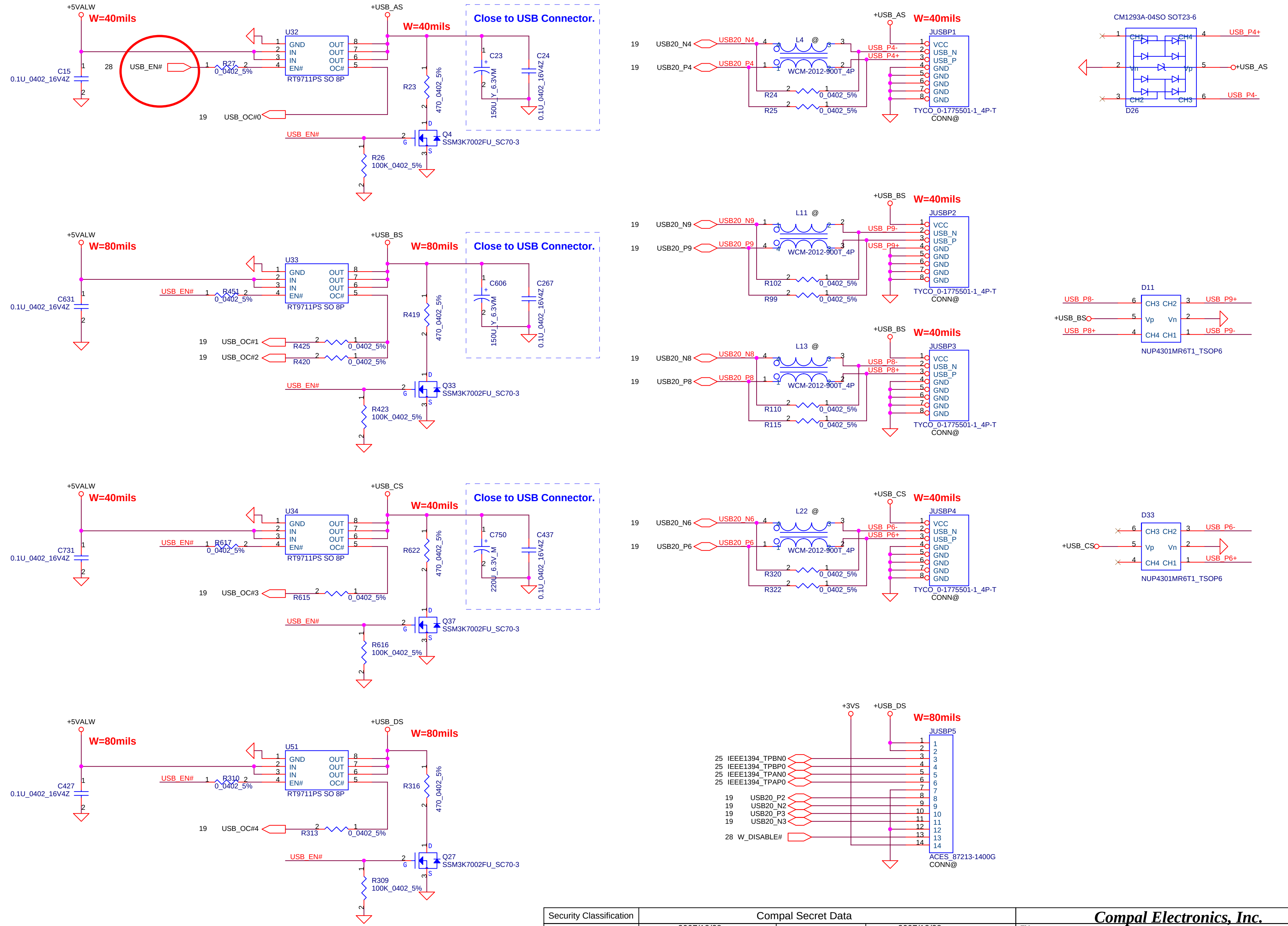


Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: *PWR_OK / BTN / KB / TP*

Size	Document Number	Rev
Custom	LA-4131P	0.2



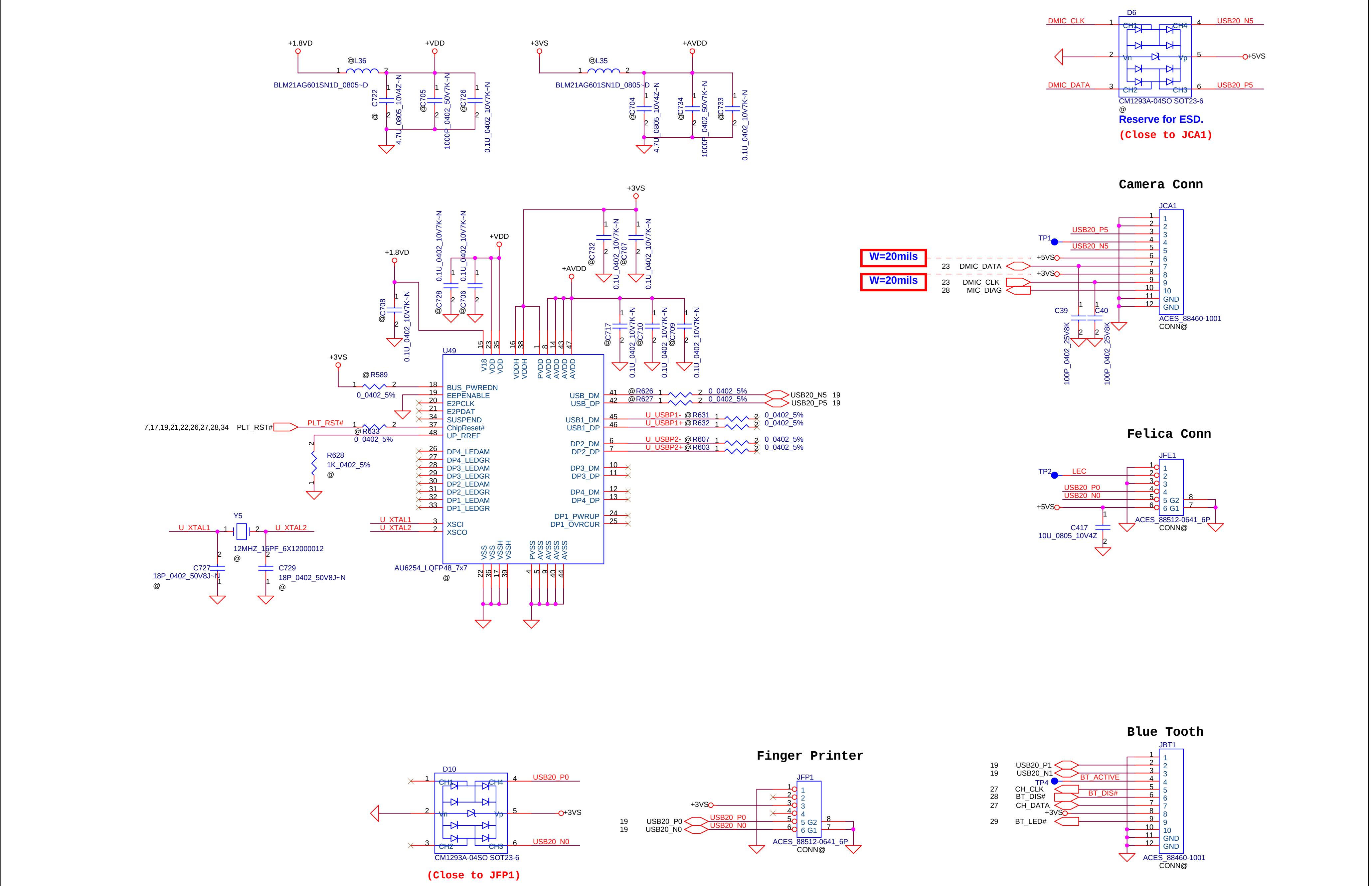
Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: **USB / 1394 Conn**

Size: Custom | Document Number: **LA-4131P** | Rev: 0.2

Date: Monday, February 18, 2008 |

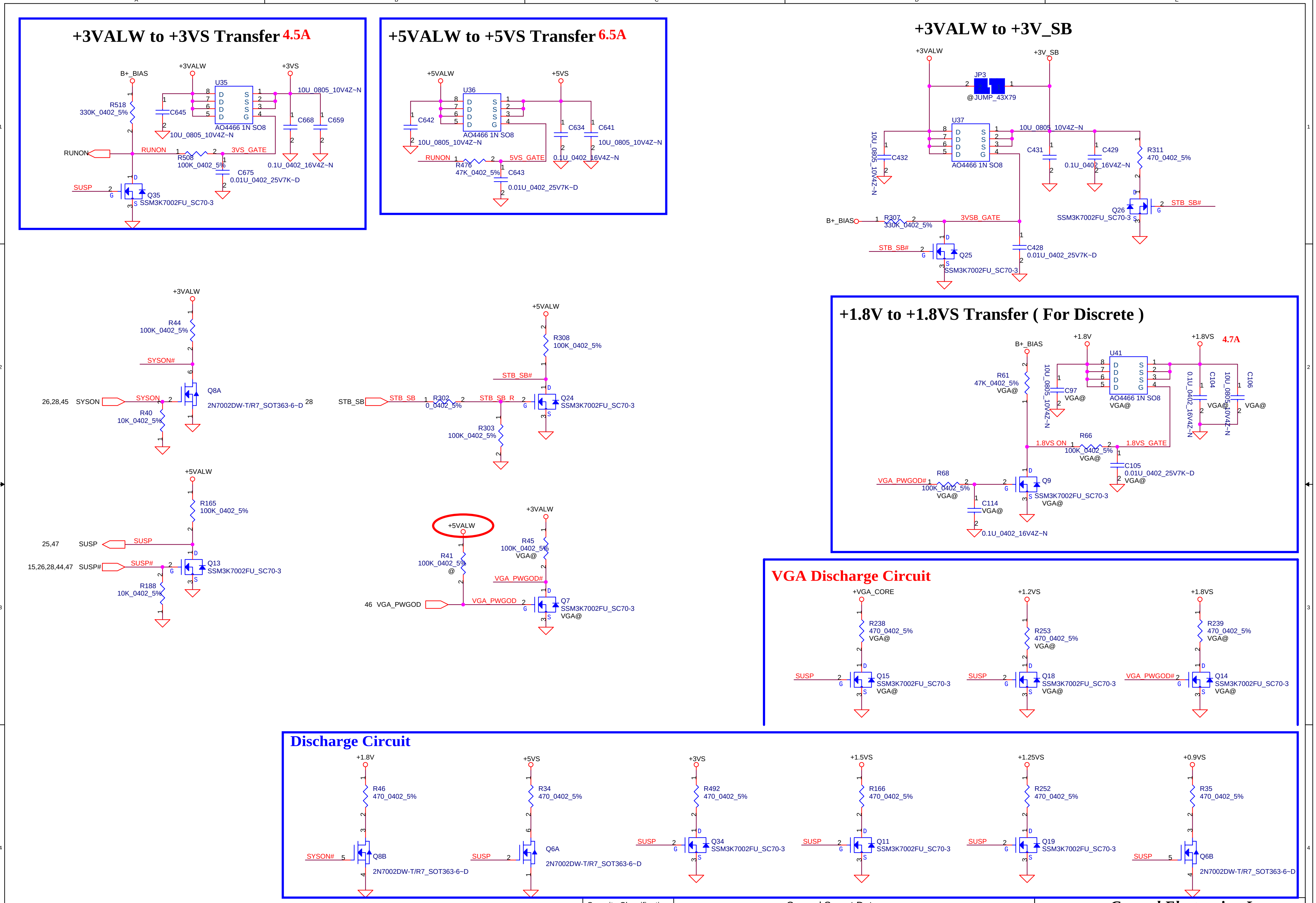


Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: *USB Hub / Camera / Felcia / FP/ BT*

Size: Custom | Document Number: LA-4131P | Rev 0.2



Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: **DC/DC Interface**

Size: Custom | Document Number: LA-4131P | Rev: 0.2

Date: Monday, February 18, 2008 | Sheet 32 of 53

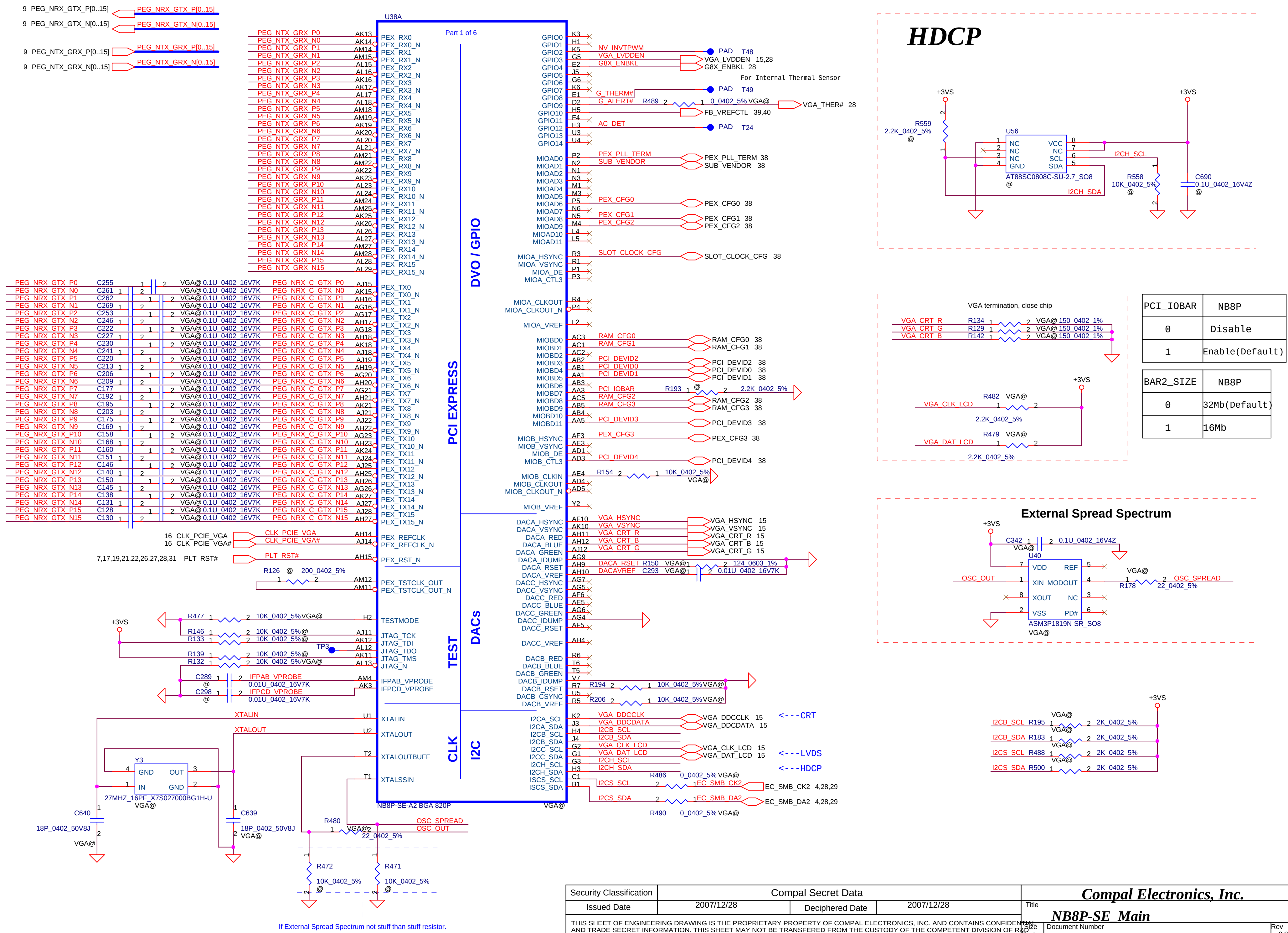
Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: **Screws**

Size: Custom | Document Number: **LA-4131P** | Rev: 0.2

Date: Monday, February 18, 2008 | Sheet 33 of 53



PCI_IOBAR	NB8P
0	Disable
1	Enable(Default)

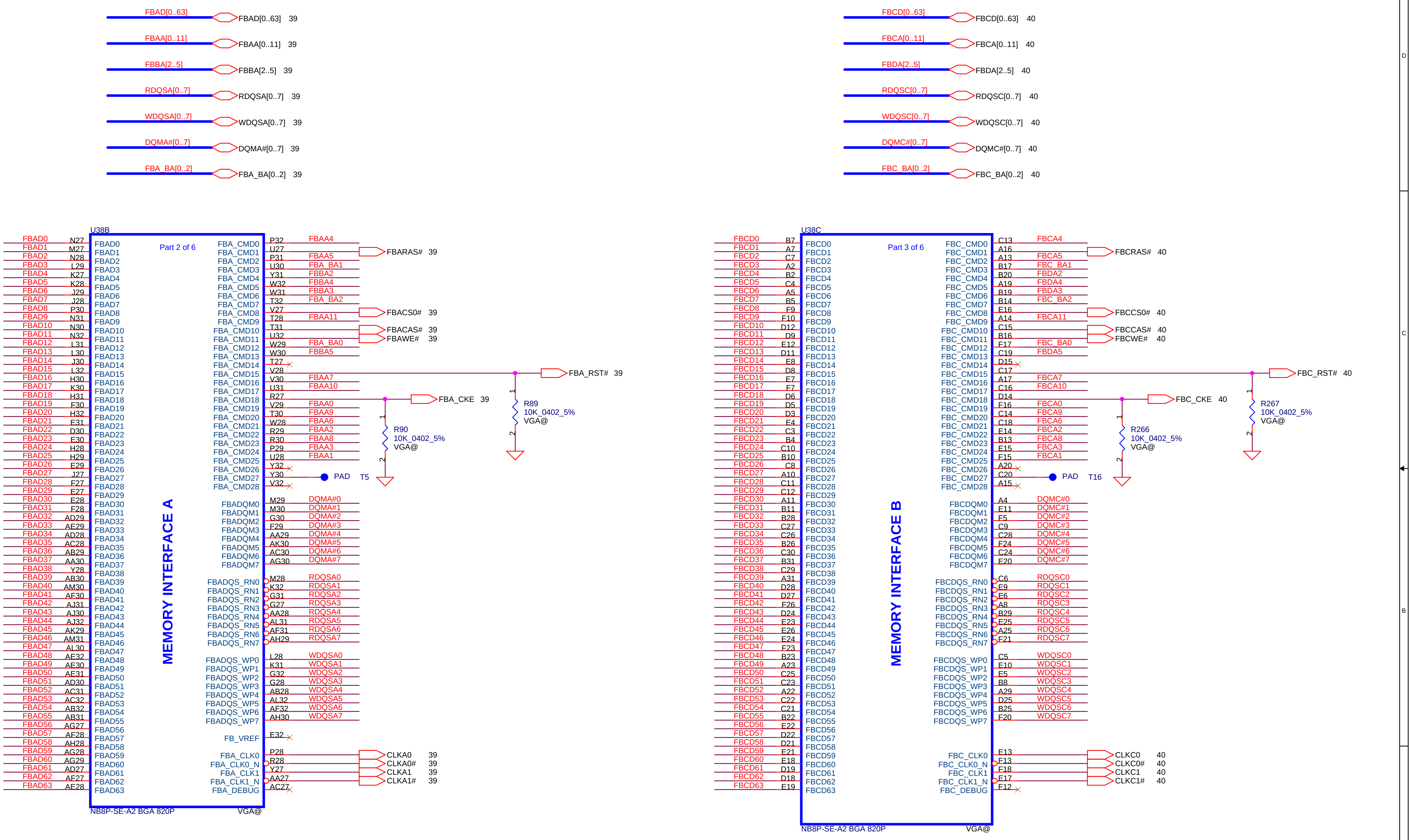
BAR2_SIZE	NB8P
0	32Mb(Default)
1	16Mb

Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: NB8P-SE_Main

Size: Custom | Document Number: LA-4131P | Rev 0.2



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/12/28	Deciphered Date	2007/12/28	Title: NB8P-SE_Memory Interface

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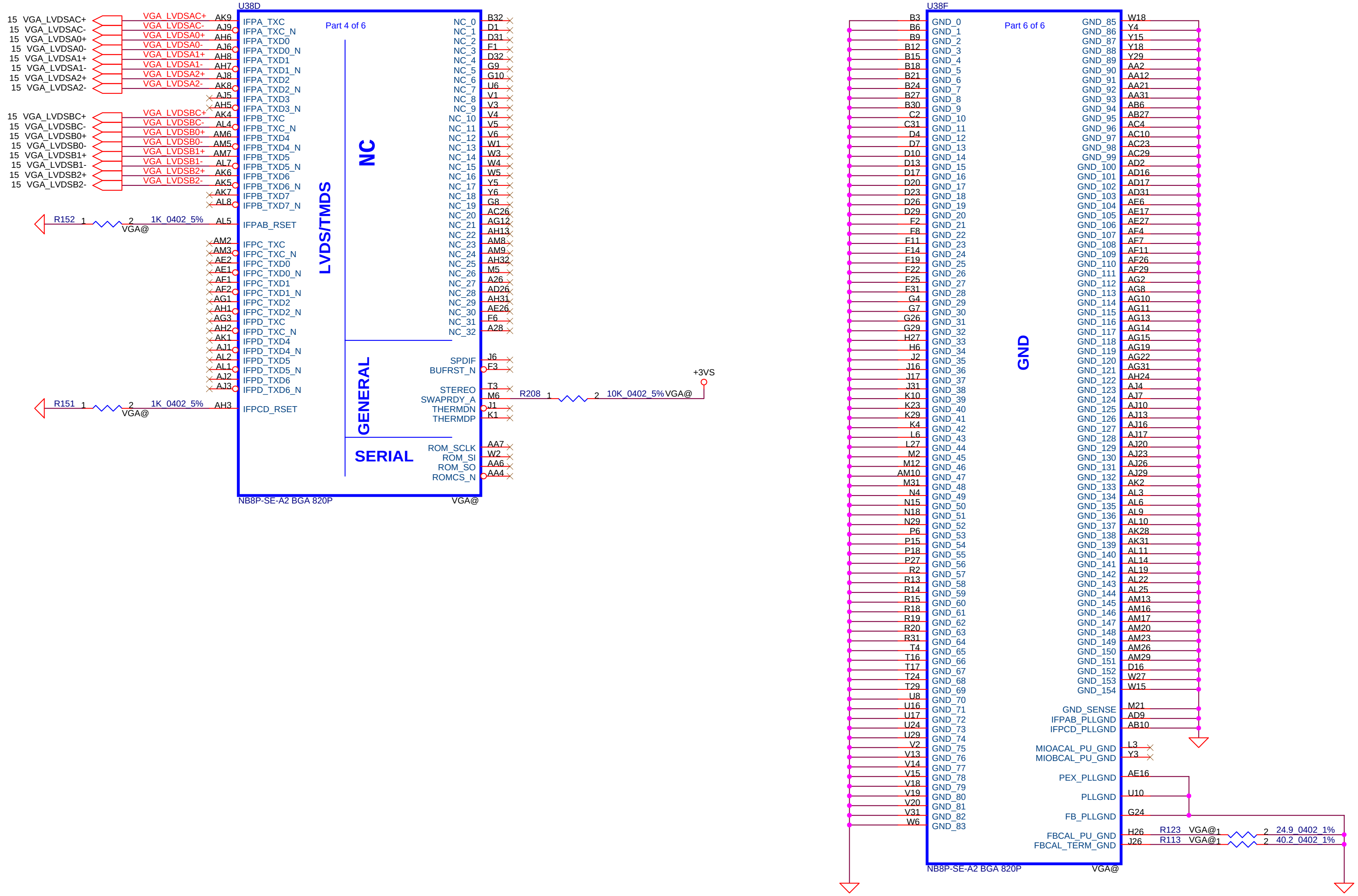
Size	Document Number	Rev
Custom	LA-4131P	0.2

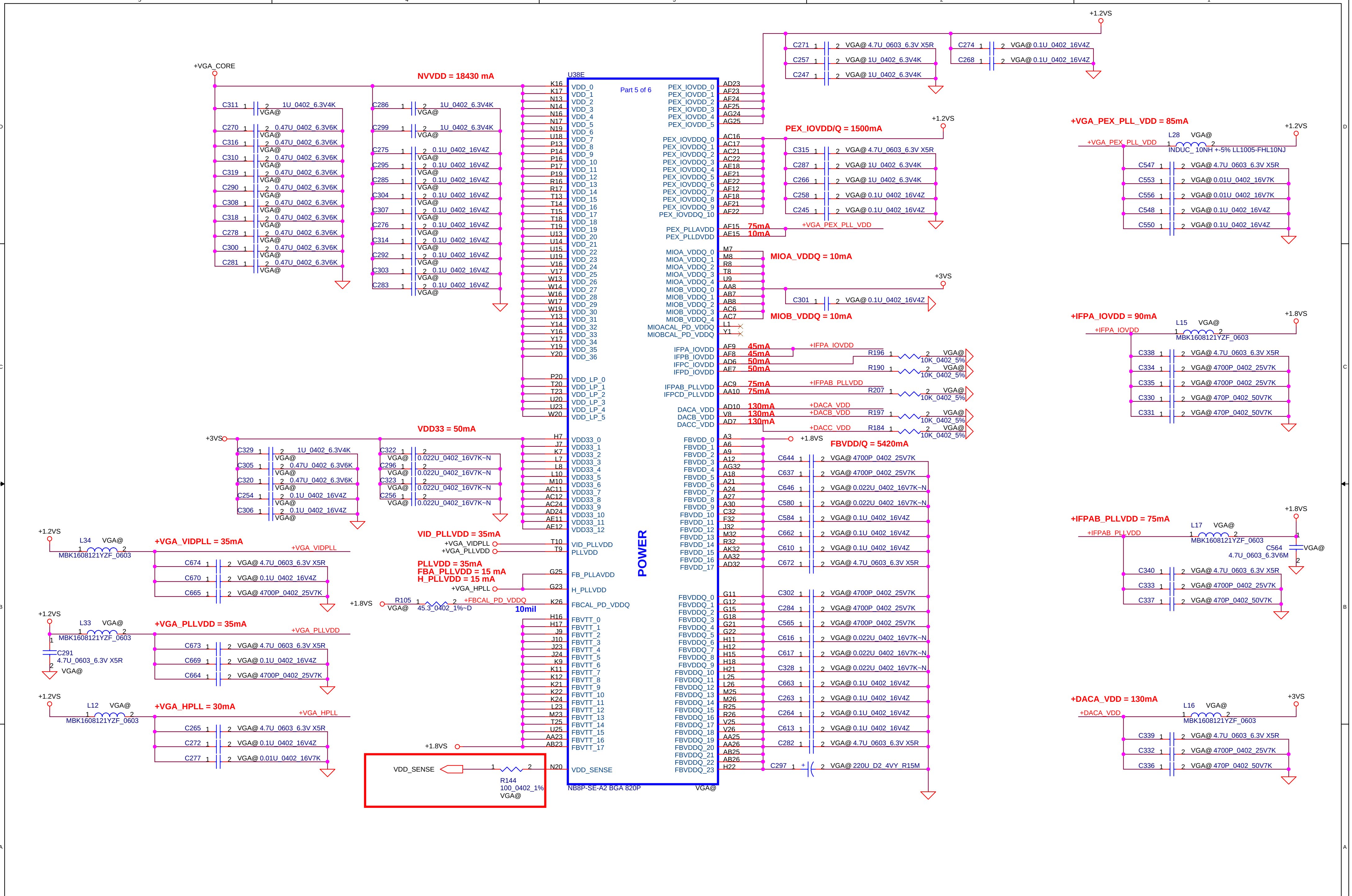
Date: Monday, February 18, 2008 | Sheet 35 of 53



Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2007/12/28	Deciphered Date 2007/12/28	Title: NB8P-SE_LVDS / TMDS / GND

Size: Custom | Document Number: LA-4131P | Rev 0.2

Date: Monday, February 18, 2008 |



Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

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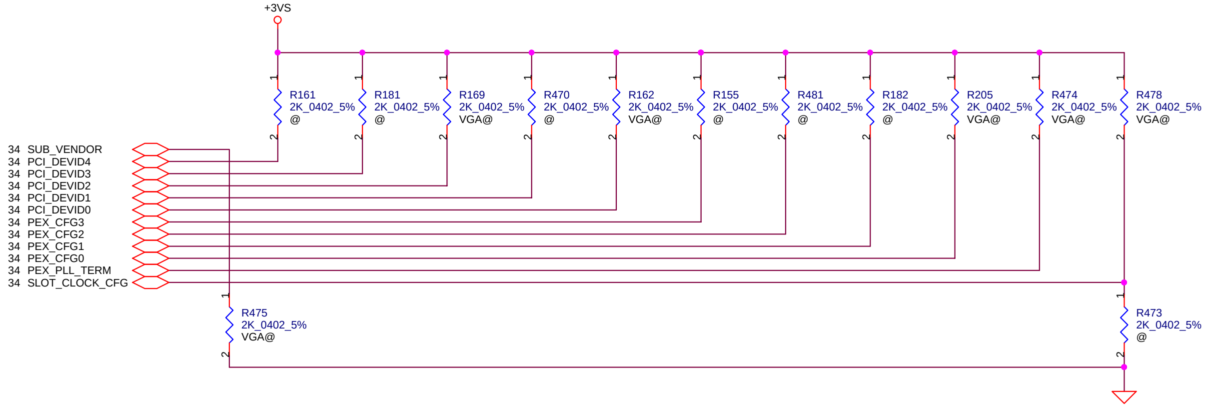
Compal Electronics, Inc.

Title: **NB8P-SE_Power**

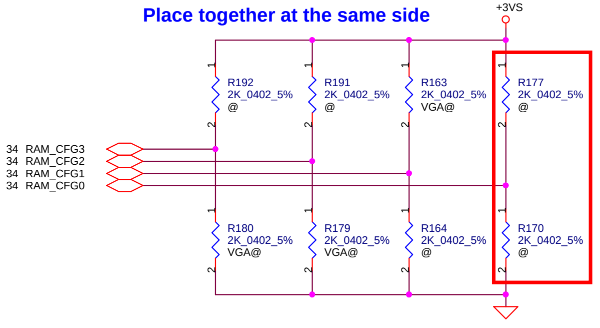
Size | Document Number: **LA-4131P** | Rev 0.2

Date: Monday, February 18, 2008 | Sheet 37 of 53



Straps	SUB_VENDOR	PCI_DEVID [4:0]	PEX_CFG[3:0]	PEX_PLL_TERM	SLOT_CLK_CFG
Description	0: BIOS not present 1: BIOS present	NB8P-GS: 0x0407 NB8P-GT: 0x0408 NB8P-SE: 0x0425	0X1	0: Enable 1: Disable	0: GPU&MCH do not share a common ref. CLK. 1: GPU&MCH share a common ref. CLK.
Setting	0: BIOS not present	NB8P-SE: 0x0425 (00101) Internal Pull down	0001: 0X1 (0001) Internal Pull down	1: Disable	1: GPU & MCH share a common ref. CLK.

Place together at the same side



RAM_CFG[3:0]

RAM_CFG3 0 = Single Rank 1 = Dual Rank	RAM_CFG2 RAM Size 0=16Mx32 1= 8Mx32	RAM_CFG[1:0] Vendor 10 = Hynix 11 = Samsung		Description NB8P-GS
0	0	0	0	
0	0	0	1	
0	0	1	0	Hynix (16M*32 4pcs = 256MB)
0	0	1	1	Samsung (16M*32 4pcs = 256MB)
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	
1	0	0	0	
1	0	0	1	
1	0	1	0	
1	0	1	1	
1	1	0	0	
1	1	0	1	
1	1	1	0	
1	1	1	1	

ZZZ

VRAM

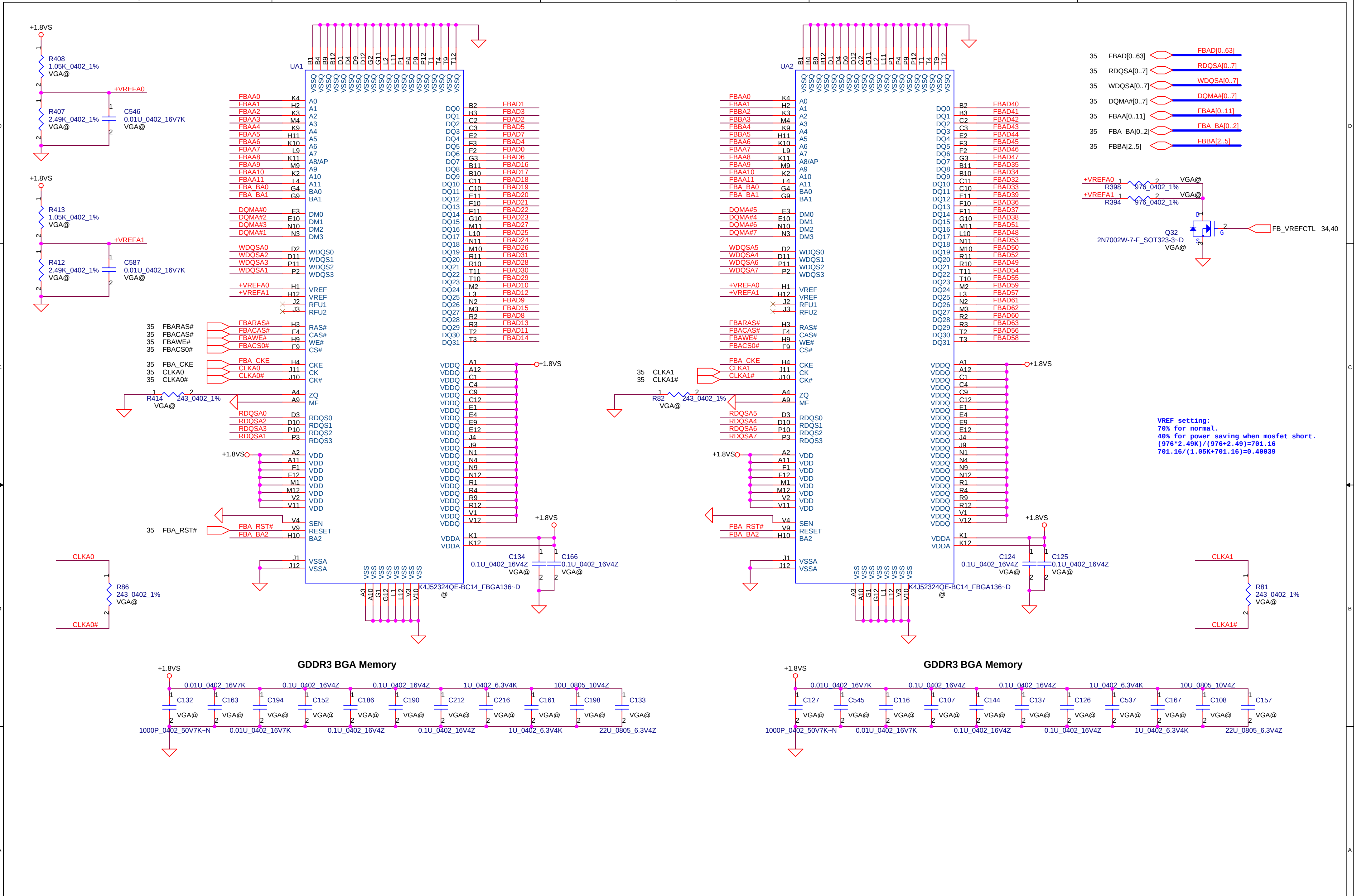
Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: NB8P-SE_Straps

Size: Custom | Document Number: LA-4131P | Rev: 0.2

Date: Monday, February 18, 2008 | Sheet 38 of 53



Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

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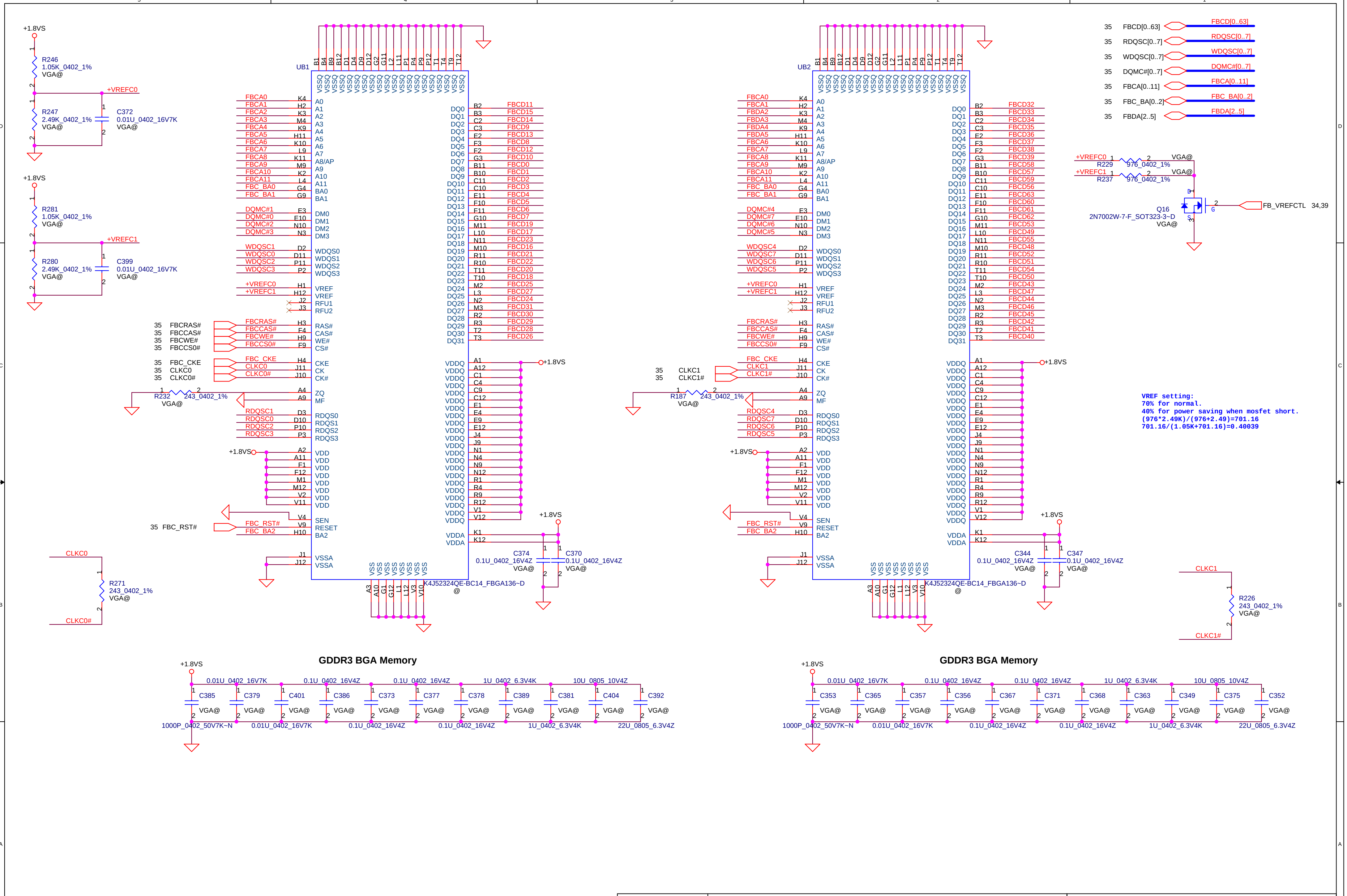
Compal Electronics, Inc.

Title: **GDDR3_A**

Size: Custom | Document Number: **LA-4131P** | Rev: 0.2

Date: Monday, February 18, 2008 | Sheet 39 of 53



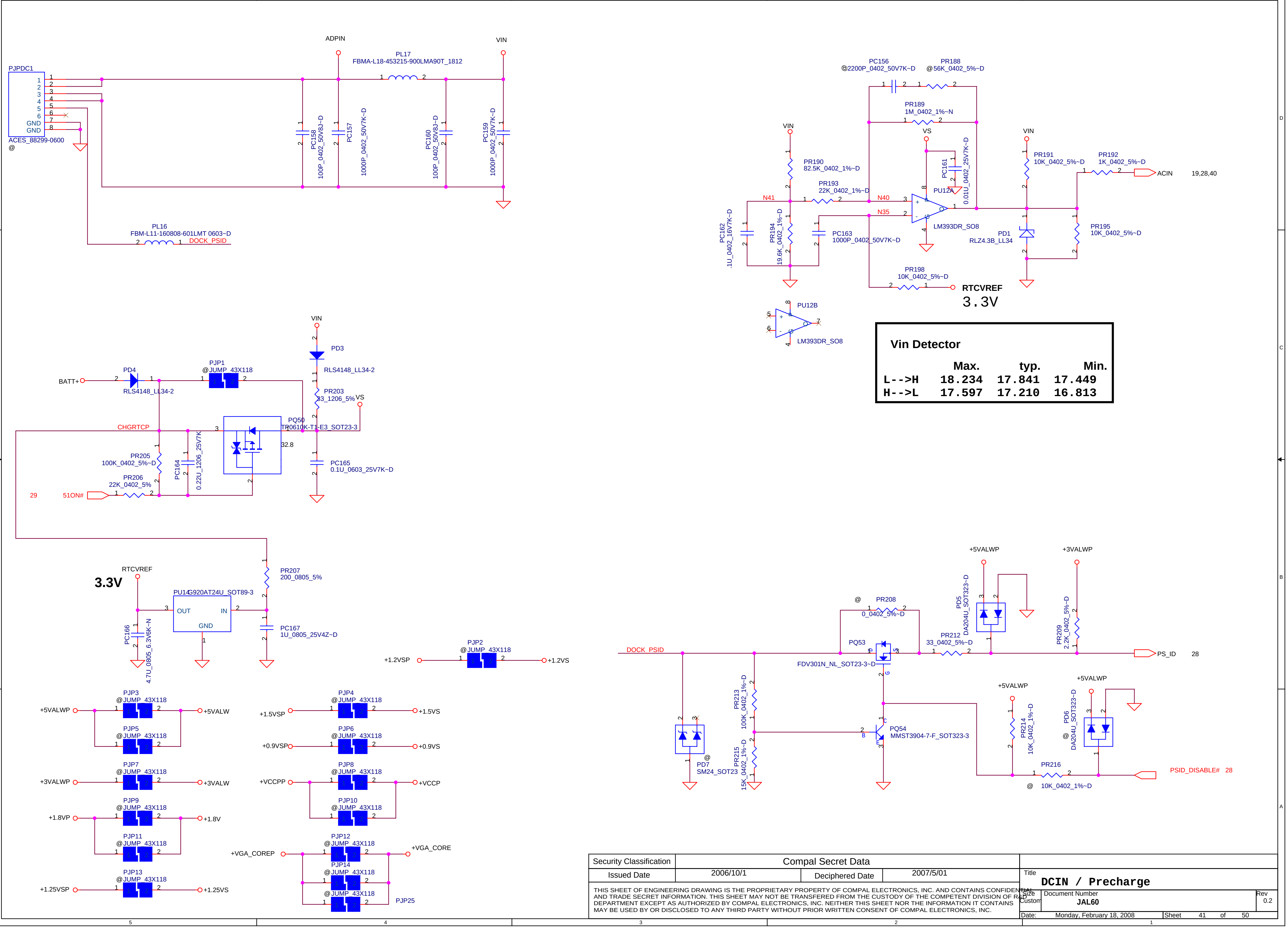
Security Classification	Compal Secret Data		
Issued Date	2007/12/28	Deciphered Date	2007/12/28

Compal Electronics, Inc.

Title: **GRRD3_B**

Size: Custom | Document Number: **LA-4131P** | Rev 0.2

Date: Monday, February 18, 2008 |



Vin Detector

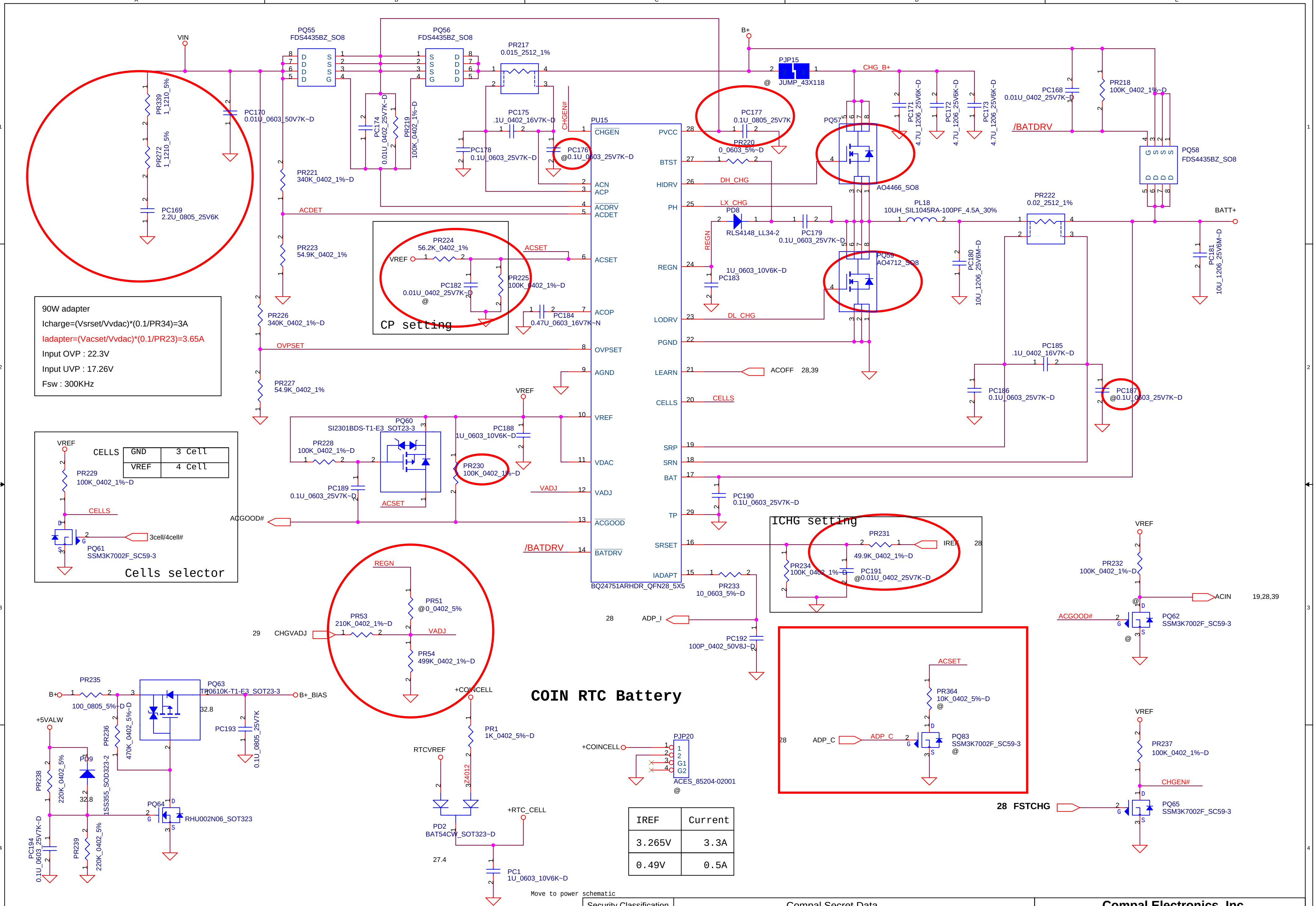
	Max.	typ.	Min.
L-->H	18.234	17.841	17.449
H-->L	17.597	17.210	16.813

Security Classification	Compal Secret Data		
Issued Date	2006/10/1	Deciphered Date	2007/5/01

Title: **DCIN / Precharge**

Size: Custom | Document Number: JAL60 | Rev: 0.2

Date: Monday, February 18, 2008 | Sheet 41 of 50



CELLS	GND	3 Cell
	VREF	4 Cell

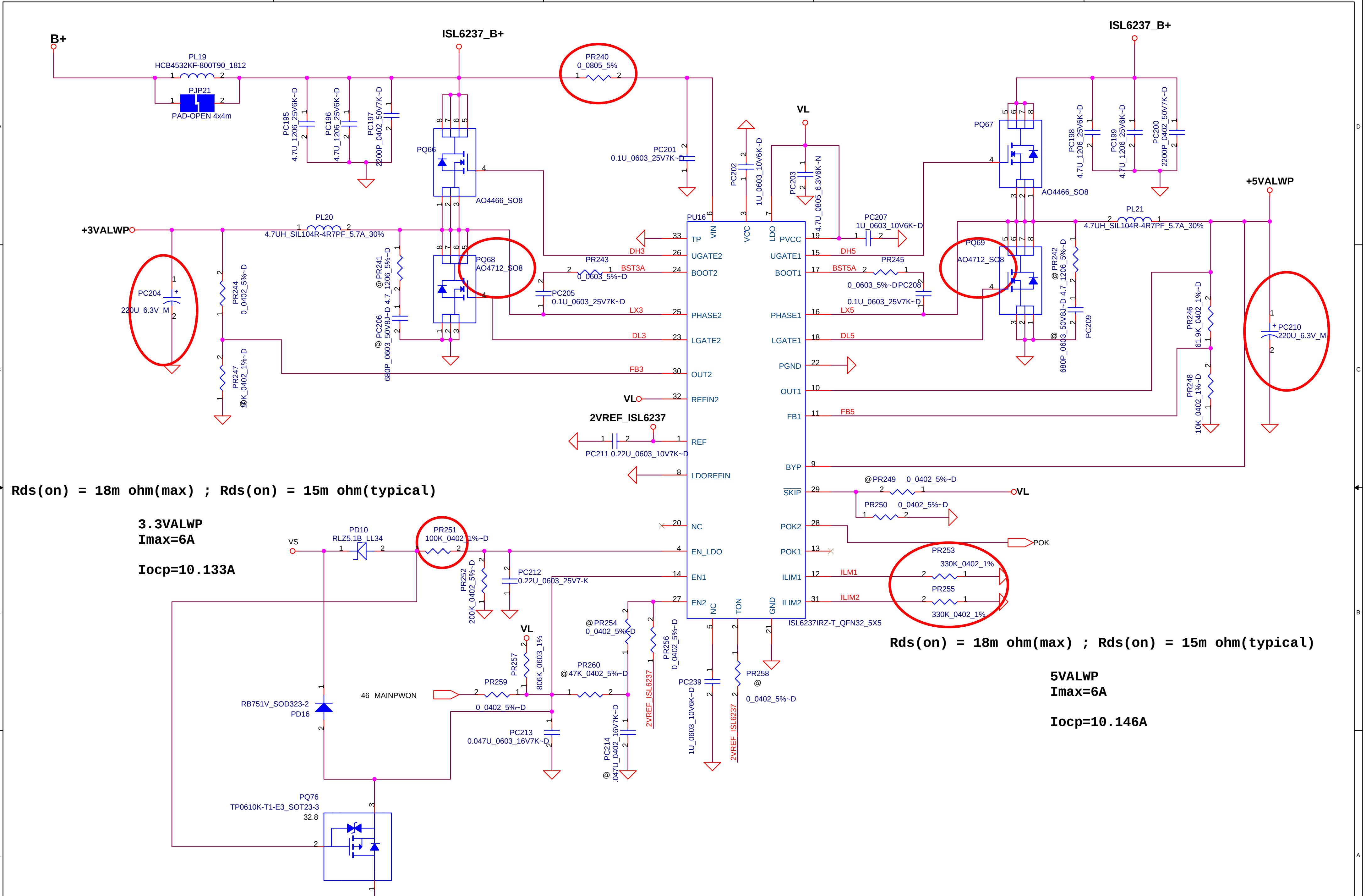
IREF	Current
3.265V	3.3A
0.49V	0.5A

Security Classification	Compal Secret Data		
Issued Date	2006/10/1	Deciphered Date	2007/5/01

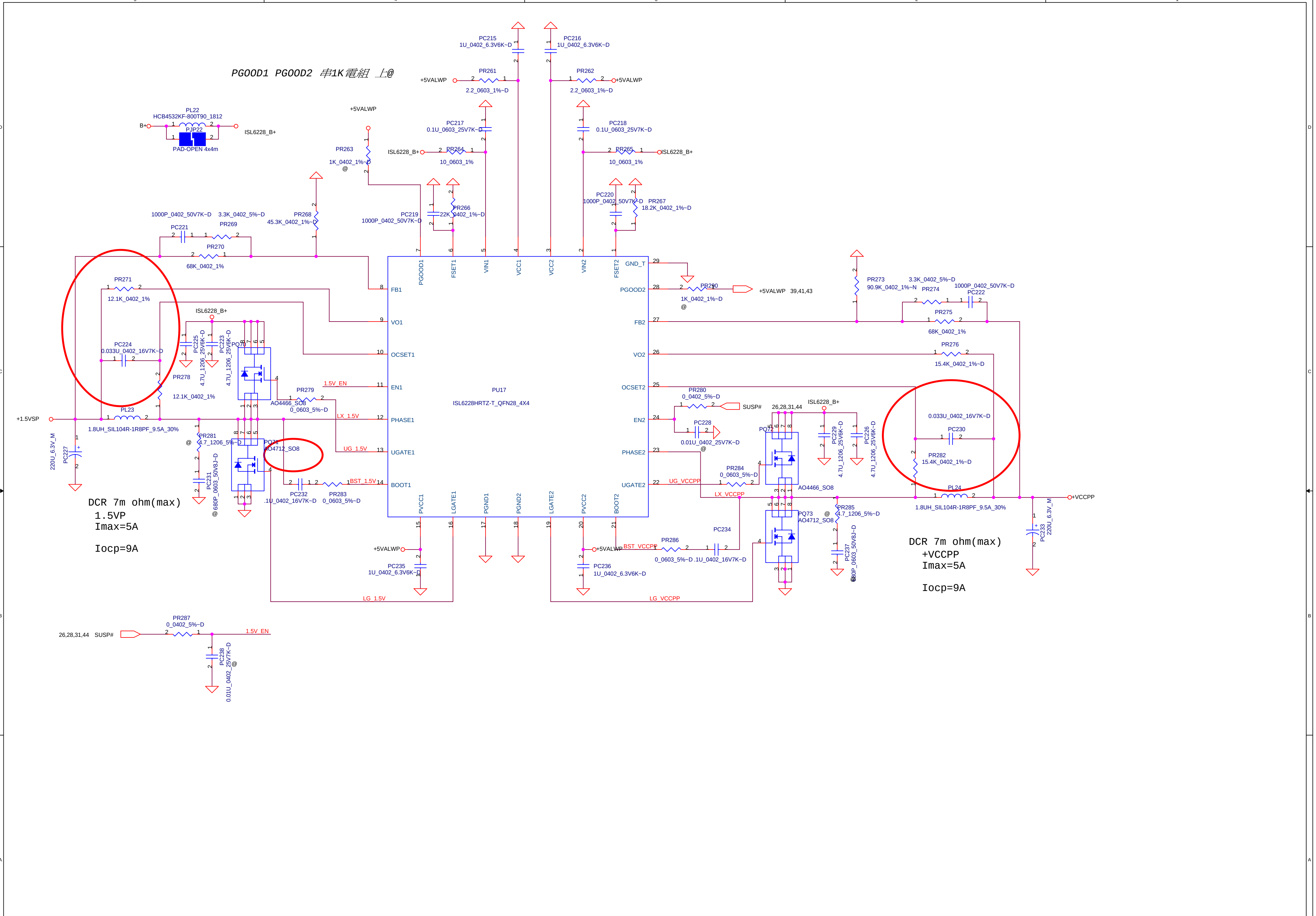
Compal Electronics, Inc.

Title: Charger

Size: B | Document Number: JAL60 **** | Rev 0.2



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2006/10/1	Deciphered Date	2007/05/30	Title: +3VALWP, +5VALWP
				Size: Custom · Document Number: JAL60 · Rev 0.2
				Date: Monday, February 18, 2008 · Sheet 43 of 50

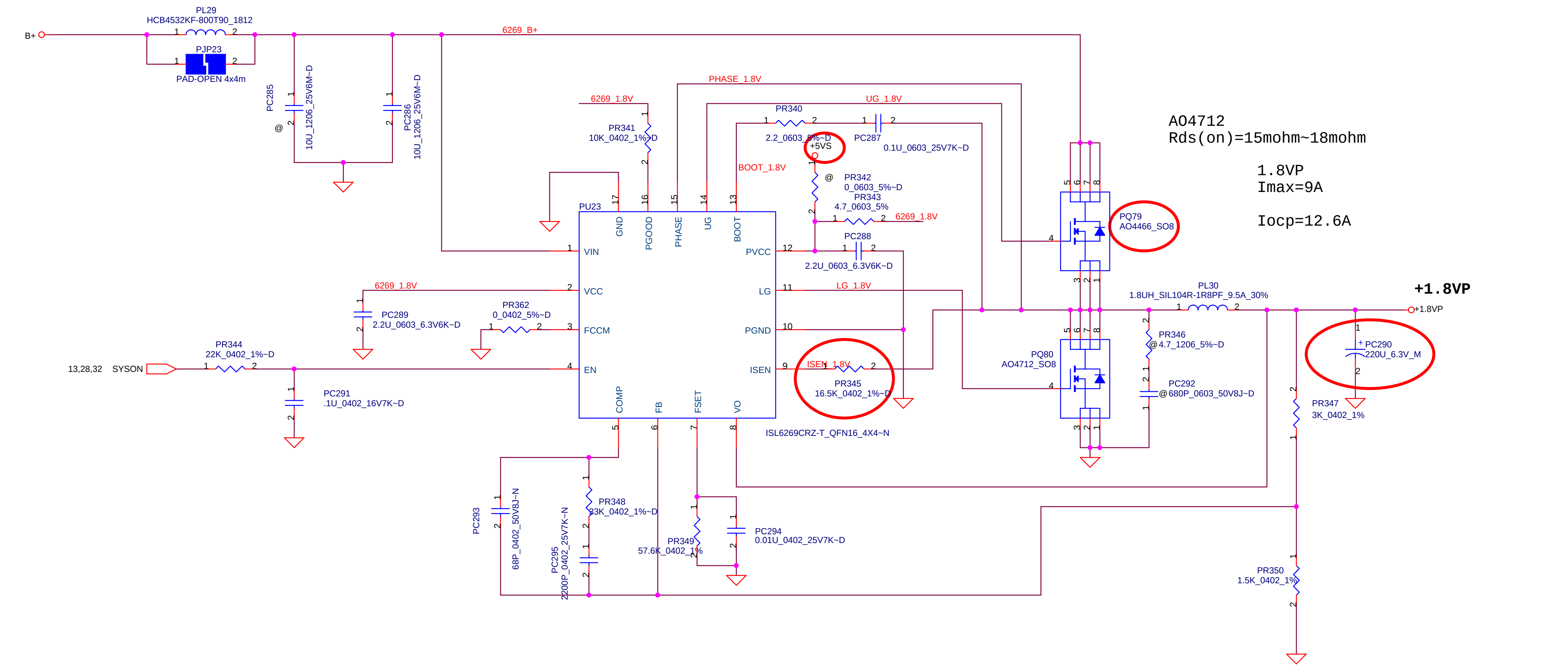


Security Classification	Compal Secret Data		
Issued Date	2006/10/1	Deciphered Date	2007/5/01

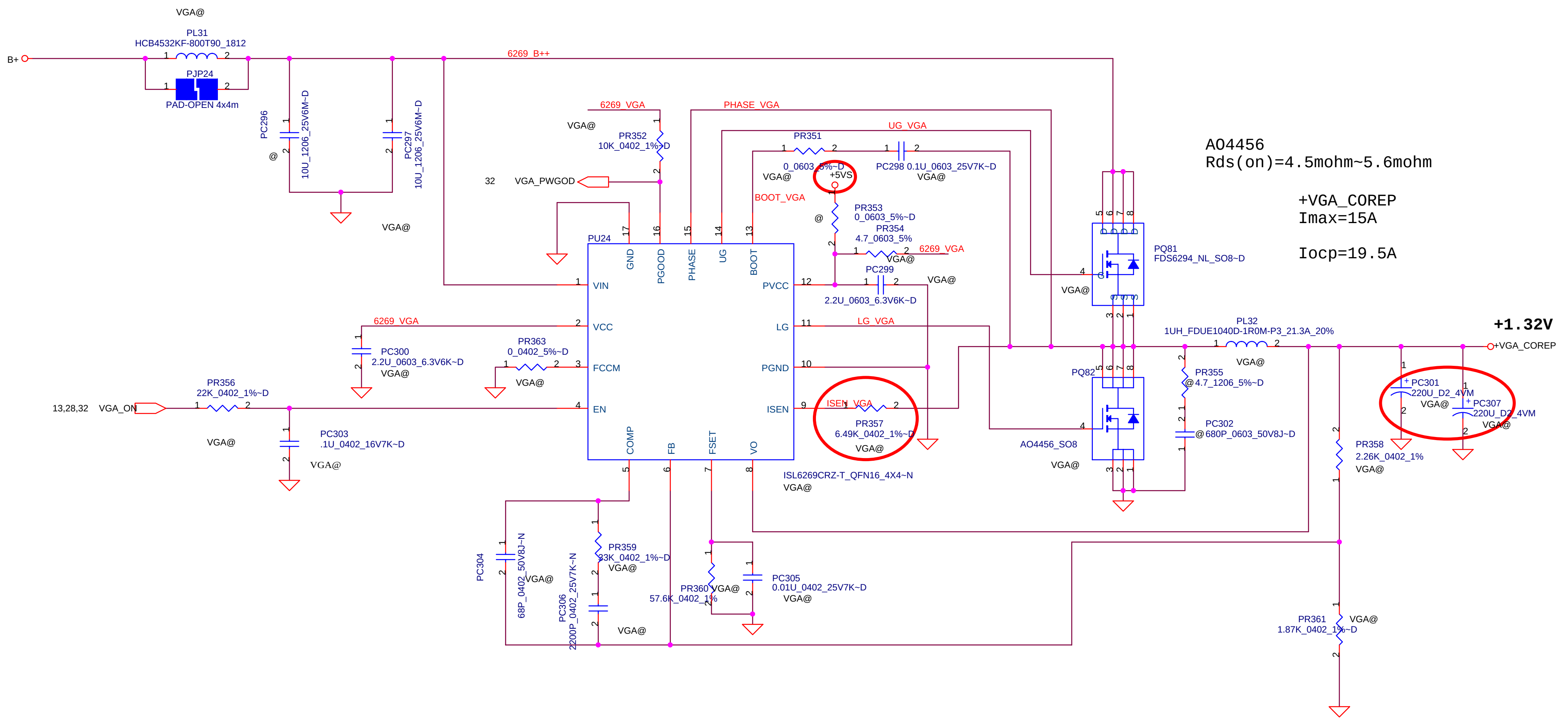
Title: +1.5VP/VCCPP

Document Number: JAL60 — Rev 0.2

Date: Monday, February 18, 2008 — Sheet 44 of 50



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2006/10/1	Deciphered Date	2007/05/30	Title: NB_CORE
				Size / Document Number: JAL70 / Rev 0.11
				Date: Monday, February 18, 2008 / Sheet 45 of 10



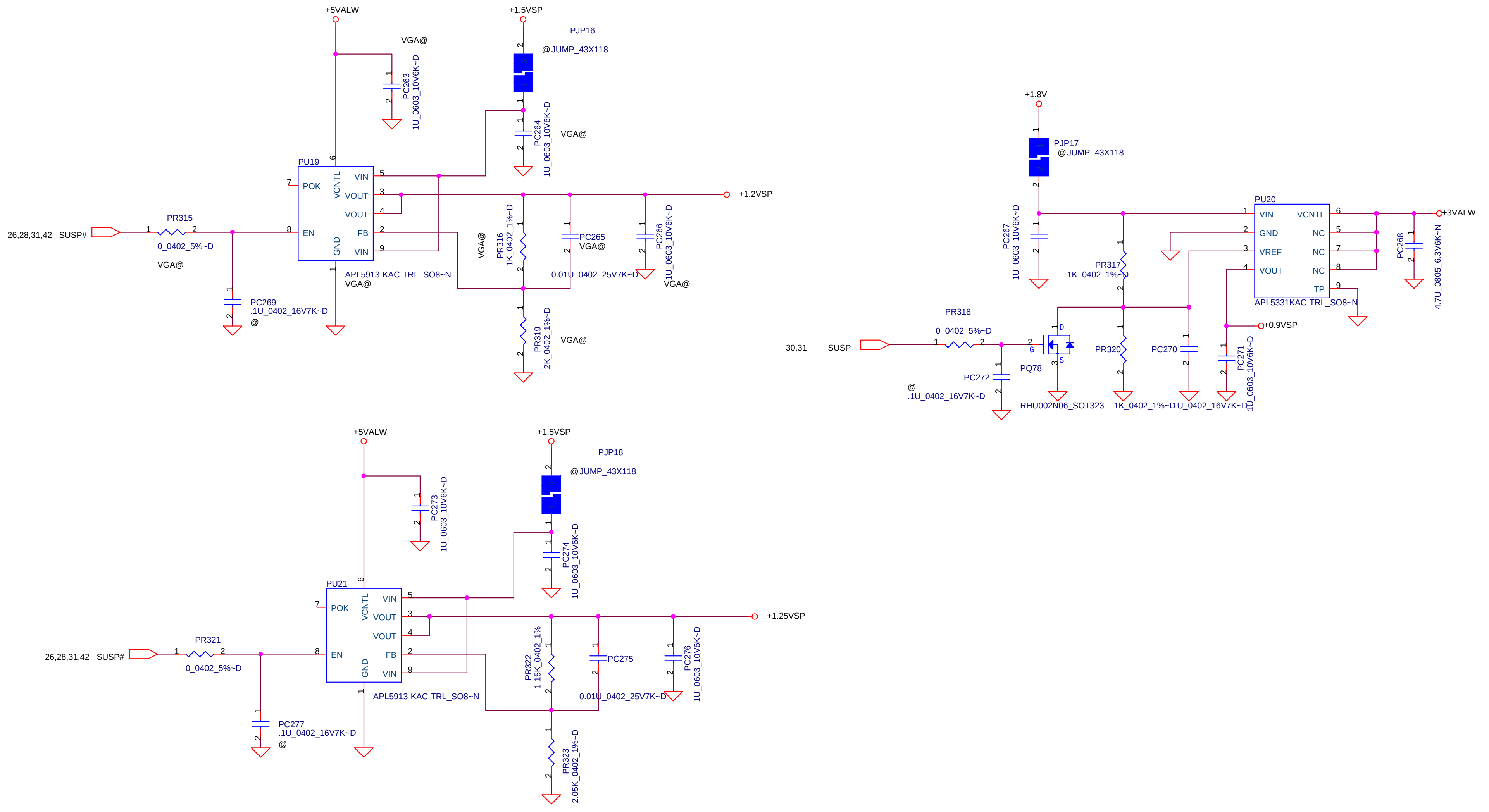
Security Classification	Compal Secret Data		
Issued Date	2006/10/1	Deciphered Date	2007/05/30

Compal Electronics, Inc.

Title: **+VGA_COREP**

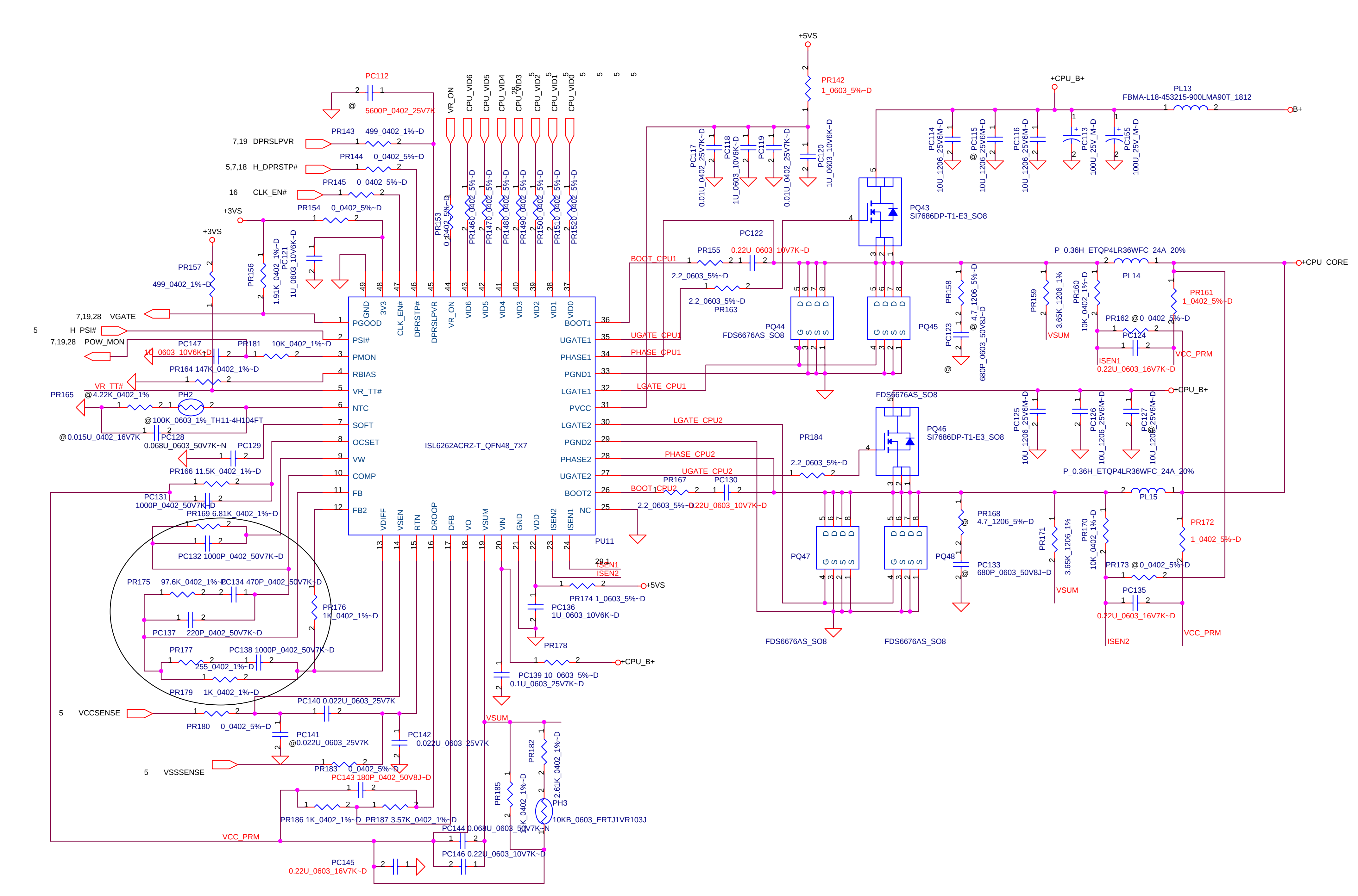
Size | Document Number: **JAL60** | Rev 0.11

Date: Monday, February 18, 2008 | Sheet 46 of 10



Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2005/10/1	Deciphered Date 2007/05/30	Title: +1.25VSP / +0.9VSP/ +1.2VSP

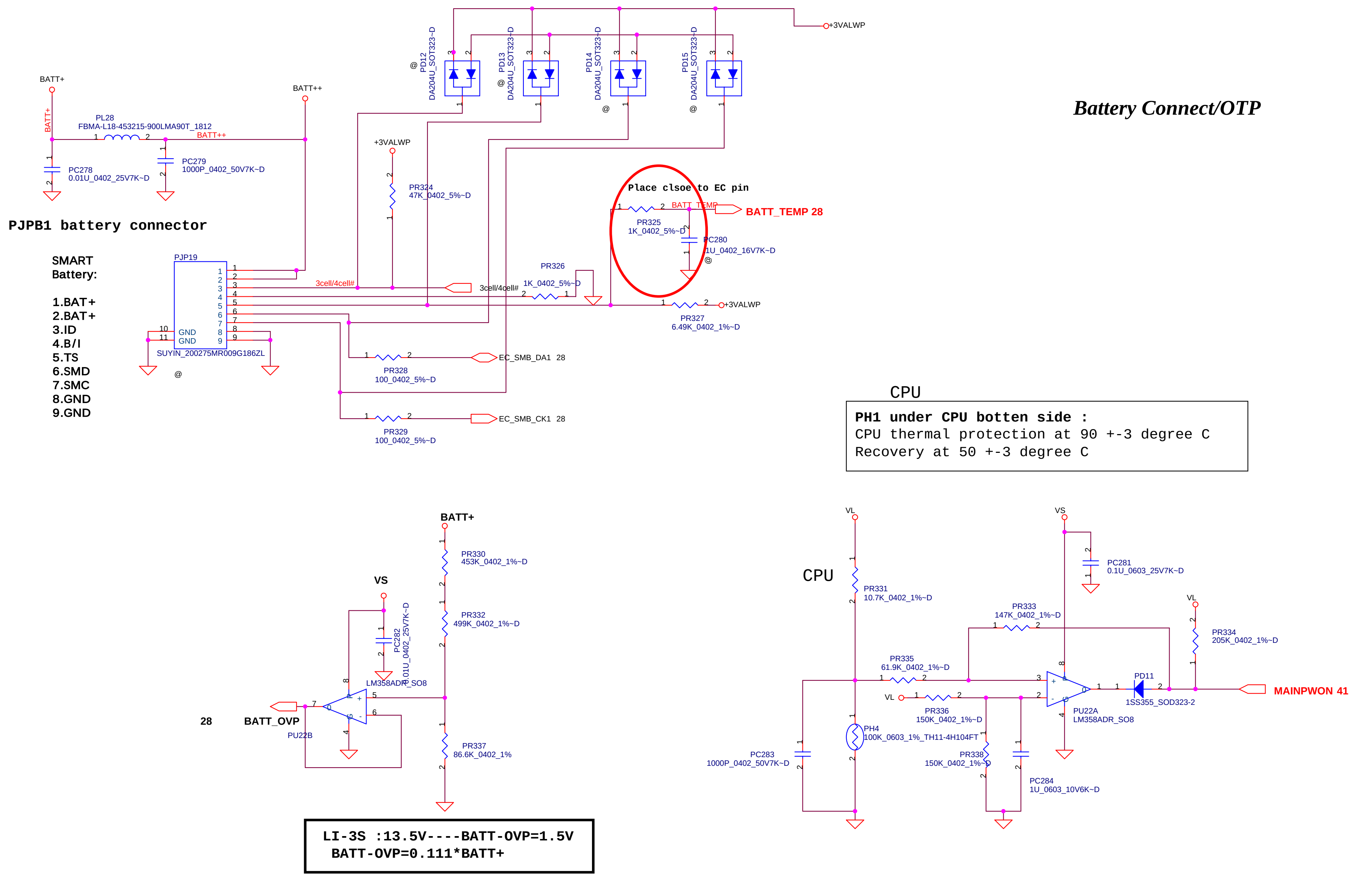
Size: Custom | Document Number: JAL60 | Rev: 0.2



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/1/15	Deciphered Date	2008/1/15	Title: +CPU_CORE

Size: Custom | Document Number: JAL60 | Rev: 0.1

Date: Monday, February 18, 2008 | Sheet 48 of 50



Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2005/10/1	Deciphered Date	2007/05/30
Title	BATTERY CONN		
Size: Custom	Document Number: JAL60		Rev 0.2
Date: Monday, February 18, 2008	Sheet 49 of 50		

Version Change List (P. I. R. List)   Page 1/1

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	ALL	ALL Page	07/11/30	DELL	base on customer request change SD SE to *L part	ALL SD* SE*	
2	P40	CHARGER	07/12/11	COMPAL	in order to meet the JETA rule,the charge voltage will be auto adjust by EC	ADD PR53(4.3K) PR54 PR51	
3	P41	+3VALWP/+5VALWP	07/12/11	COMPAL	When battery only and 4s shut down the system can not turn off the 3valwp and 5valwp	ADD PD16 (RB751V),PQ76(TP0610K)	
4	P41	+3VALWP/+5VALWP	07/12/11	COMPAL	in order to trial run ISL6237 2'nd source,reserve PC239 in ISL6237 PIN5.	ADD PC239 (1U 0603 10V)	
5	P44	+VGA_CORE	07/12/11	COMPAL	Base on HW power budger,change the VGA_CORE high low side MOS and choke.	change PQ81 from AO4466 to FDS6294,PQ82 from AO4712 to AO4456,PL32 from 1.8uH 9.5A to 1uH 21.3A	
6	P39	DC-IN / Precharge	07/12/11	COMPAL	base on customer spec no need CHARGE RTC, delete CHARGE RTC limit resister	Del PR210,PR211	
7	P46	CPU_CORE	07/12/14	COMPAL	EMI request to increase resister in CPU_CORE low side mos gate pin	ADD PR163(2.2 ohm),PR184(2.2ohm)	
8	P43	1.8VP	07/12/14	COMPAL	EMI request to increase resister in 1.8VP BOOT pin	Change PR340 from 0 ohm to 2.2 ohm	
9	P40	CHARGER	08/01/04	COMPAL	disable BQ24751 VIN detecte function	CHANGE PQ62 to @ and PR232 to @	
10	P40	CHARGER	08/02/12	COMPAL	EC have drop volotage in CHGVADJ,it will make the charge voltage not correct	CHANGE PR53 from 4.3k to 210k and PR54 from 10k to 499k	
11	P44	+VGA_CORE	08/02/12	COMPAL	Base on HW power budger,change the VGA_CORE output capacity	change PC301,PC307 from SF22001M300 S ELE CAP 220U 6.3V M F60(6.3X5.7) PXC to SGA20221150 S POLY C 220U 4V M D2 PSL LESR15M H1.9	
12							

Security Classification	Compal Secret Data		
Issued Date	2007/1/15	Deciphered Date	2008/1/15

Compal Electronics, Inc.

Title: PW PIR-1

Size: Custom | Document Number: JAL60 | Rev: 0.1

Version Change List (P. I. R. List) Page 1/1

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	28	EC_KB926/Reed Switch/BIOS/TPM	2007/11/14	Compal_HW	Change Board ID.	Change R459 to 8.2K ohm.	0.2
2	38	NB8P-SE_Straps	2007/11/14	Compal_HW	There is display issue on Discrete platform.	Not stuff R784.	0.2
3	32	DC/DC Interface	2007/11/19	Compal_HW	Sometime system can't detect USB device.	Change R522 pull high to +5VALW. (for SUSP)	0.2
4	15	CRT Conn/LCD Conn	2007/12/03	Compal_HW	Support BIST and LCD detect function for LCD panel.	Add nets: LCD_DET# , BIST and Modify the pin definition of JP4.	0.2
5	23	Audio Codec_ALC268	2007/12/03	Compal_EMI	There are EMI issue on DMIC_DATA and DMIC_CLK.	Add R933, R934, C1189, C1190 for EMI.	0.2
6	25	OZ129_Card Reader/1394	2007/12/03	Compal_HW	OZ129 has power sequence and leakage issue.	Update the circuit of OZ129 on Page 25.	0.2
7	28	EC_KB926/Reed Switch/BIOS/TPM	2007/12/03	Compal_HW	Can't Wake up on LAN.	Change R464 pull high to +3VALW for WOL.	0.2
8	28	EC_KB926/Reed Switch/BIOS/TPM	2007/12/03	Compal_HW	Support Wake up from USB Port and replace USB HUB port arrangement.	Add USB_EN# to enable USB power swith.	0.2
9	29	PWR_OK/BTN/KB/TP	2007/12/13	Compal_HW	51ON# shout down issue.	Change R493 pull down to GND and del R494.	0.2
10	29	PWR_OK/BTN/KB/TP	2007/12/03	Compal_HW	Int. Keyboard can't work.	Modify the pin definition of JKB1.	0.2
11	30	USB/1394 Conn	2007/12/03	Compal_HW	Support Wake up from USB Port.	1.U32, U33, U34, U51 change input voltage to +5VALW. 2.JUSBP1~5 change USB differential pair from ICH8M.	0.2
12	32	DC/DC Interface	2007/12/03	Compal_HW	VGA_PWGOD sequence issue.	1.Change R551 pull high to +5VALW and don't stuff R551. 2.Change VGA_PWGOD to PR352 pin2.	0.2
13	15	CRT Conn/LCD Conn	2007/12/13	Compal_HW	Dell 17" LCD Panel gray color issue.	Add Q83, Q84, R942, R943, C1191~C1194 for Dell 17" panel.	0.2
14	22	Realtek RTL8111C-GR	2007/12/13	Compal_EMI	These is EMI issue on Transformer.	Add R945~R952 and C1195~C1198 for EMI.	0.2
15	22	Realtek RTL8111C-GR	2007/12/18	Compal_HW	RJ-45: LED support "link" and "activity" status.	1.Change the connector of JLAN1. 2.Add D41, D42, D43, D44, R953, R954, R955.	0.2
16	15	CRT Conn/LCD Conn	2007/12/19	Compal_EMI	EMI' request.	Del CRT_GND, R105, R107 for EMI.	0.2
17	31	USB Hub/Camera/Felcia/FP/BT	2007/12/24	Compal_HW	Finfer Print assermble FFC.	Modify the definition of JFP1.	
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Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/12/28	Deciphered Date	2007/12/28	Title: EE_PIR-1
				Size: Custom; Document Number: LA-4131P; Rev 0.2
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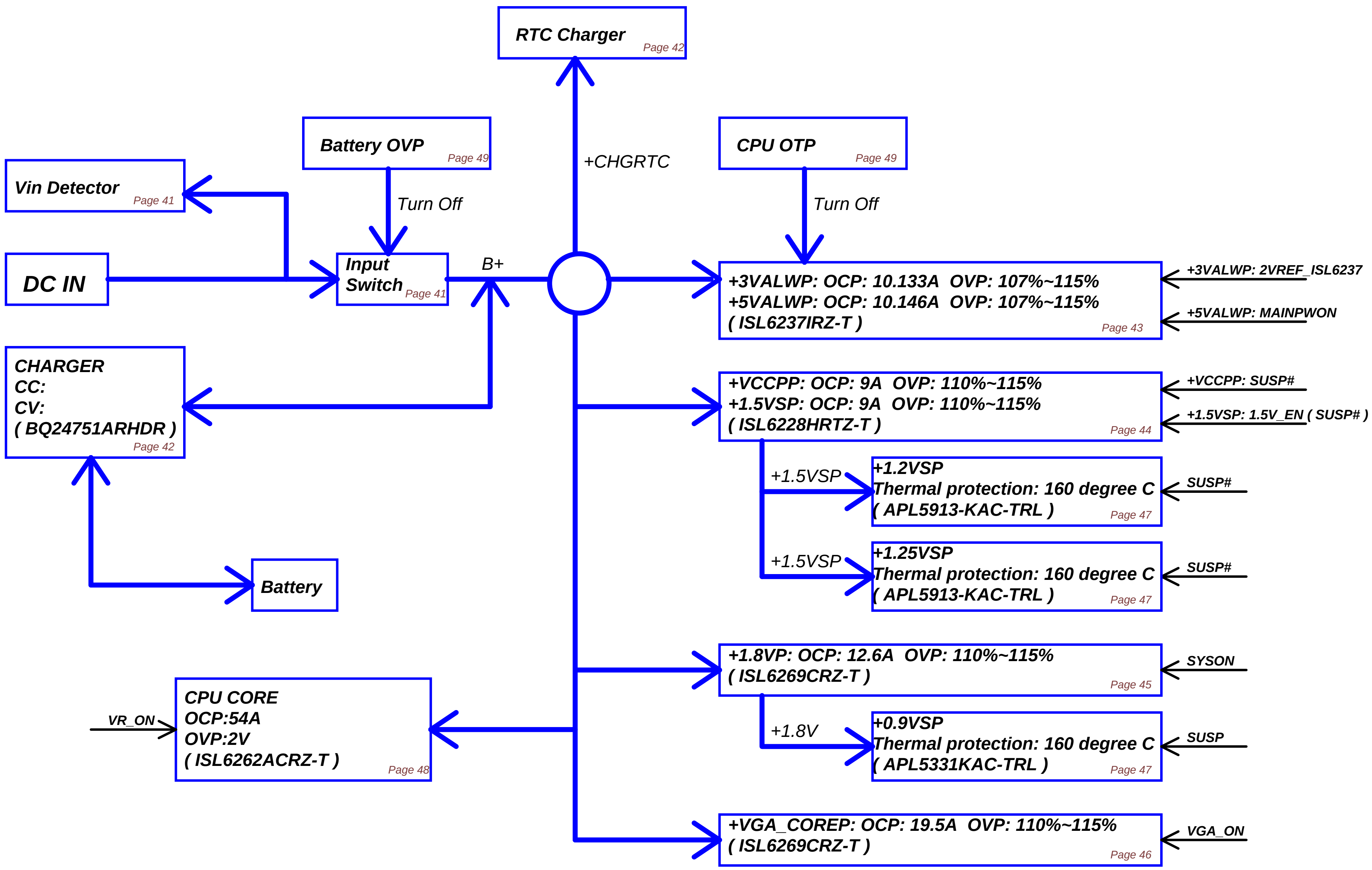
JAL60 Power Block



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Title: Power Block

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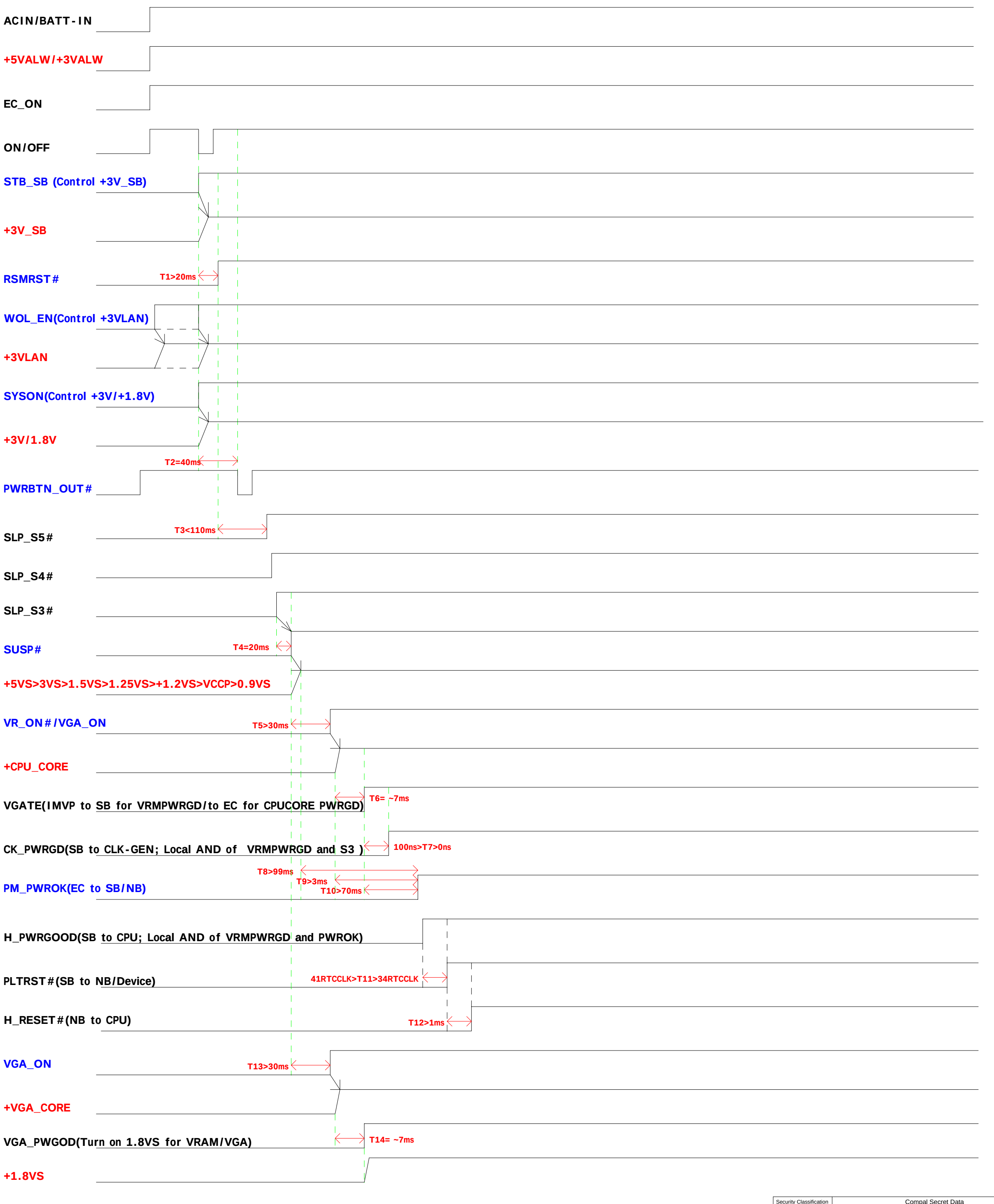

Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/12/28	Deciphered Date	2007/12/28	Title: Power On Sequence
				Size: Custom; Document Number: LA-4131P; Rev: 0.2
				Date: Monday, February 18, 2008; Sheet 53 of 53